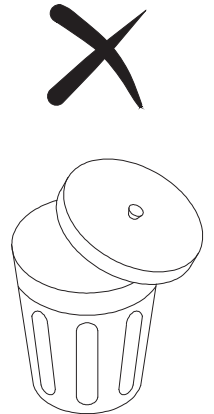
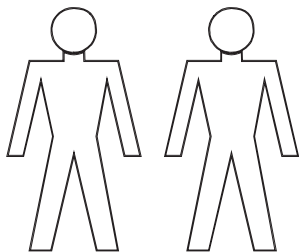
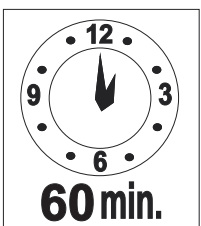
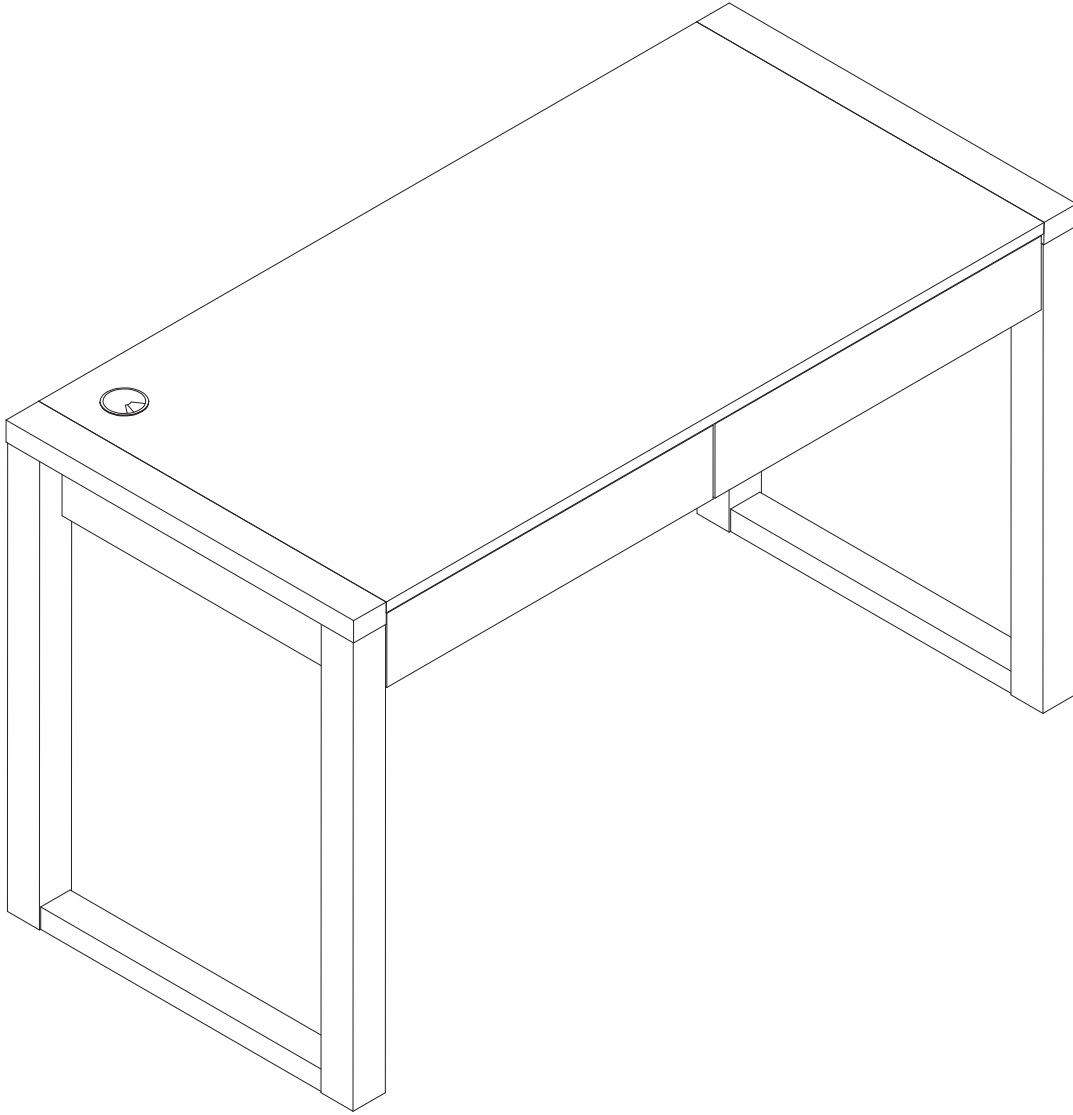
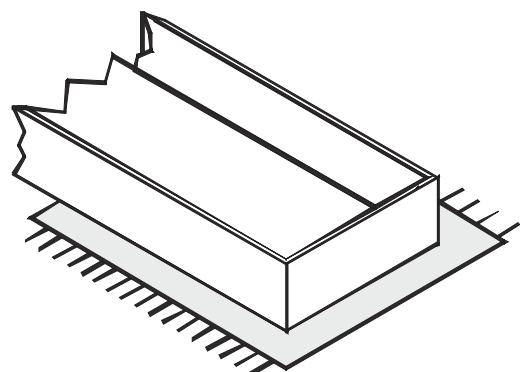
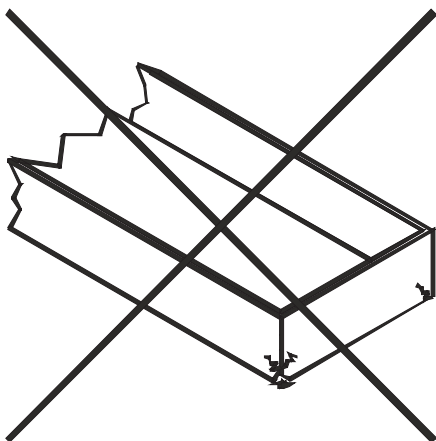
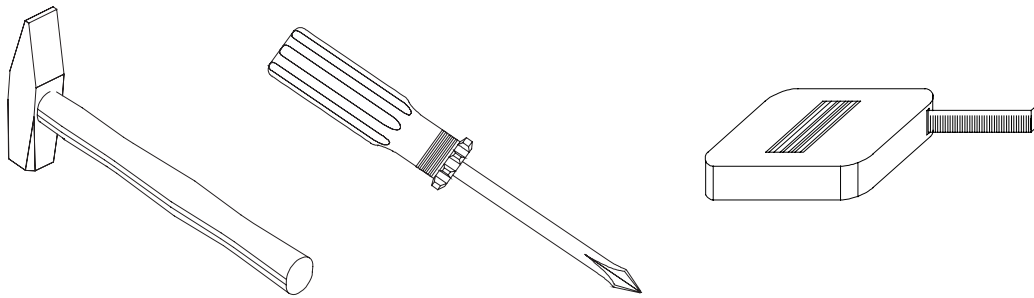
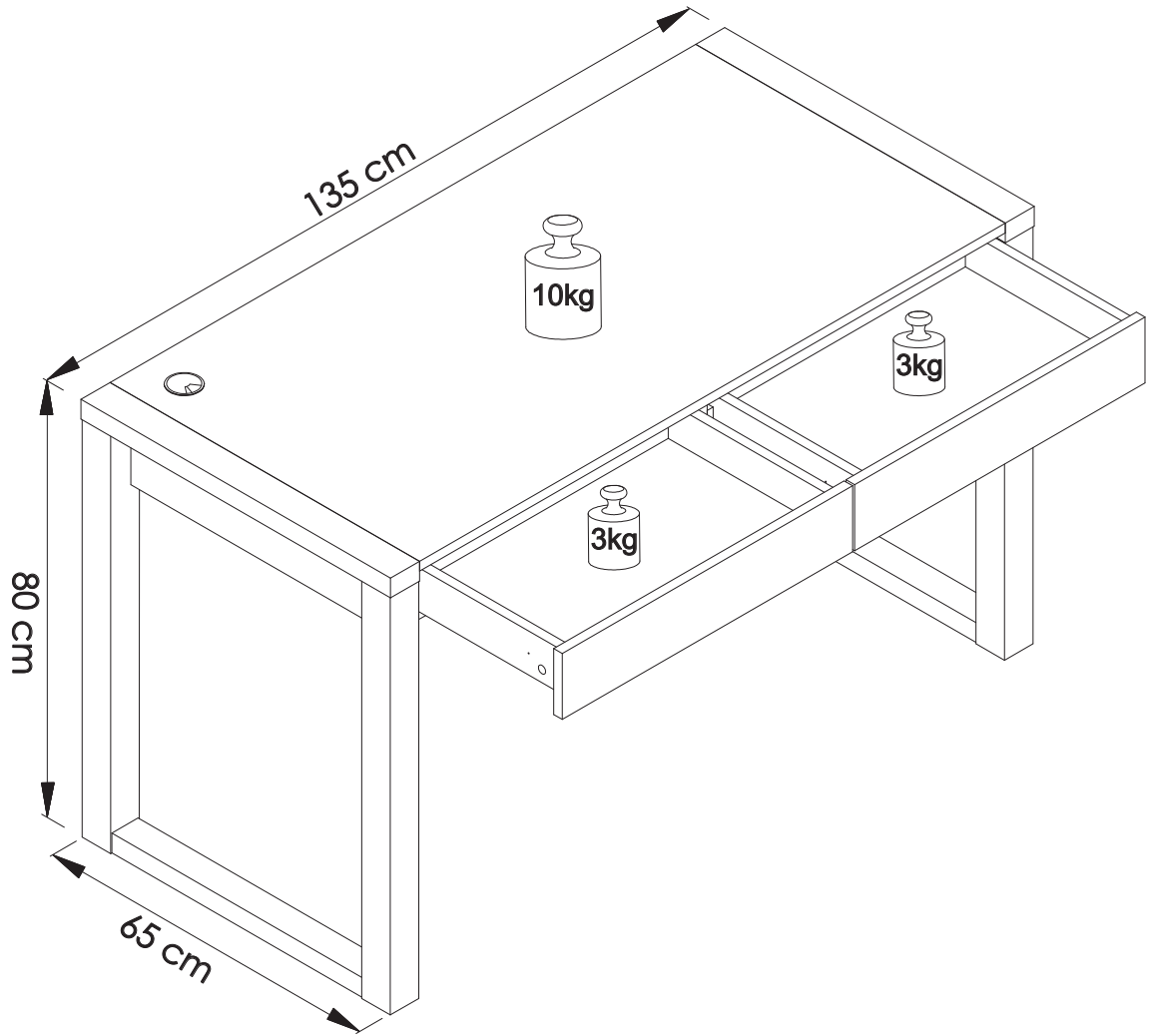
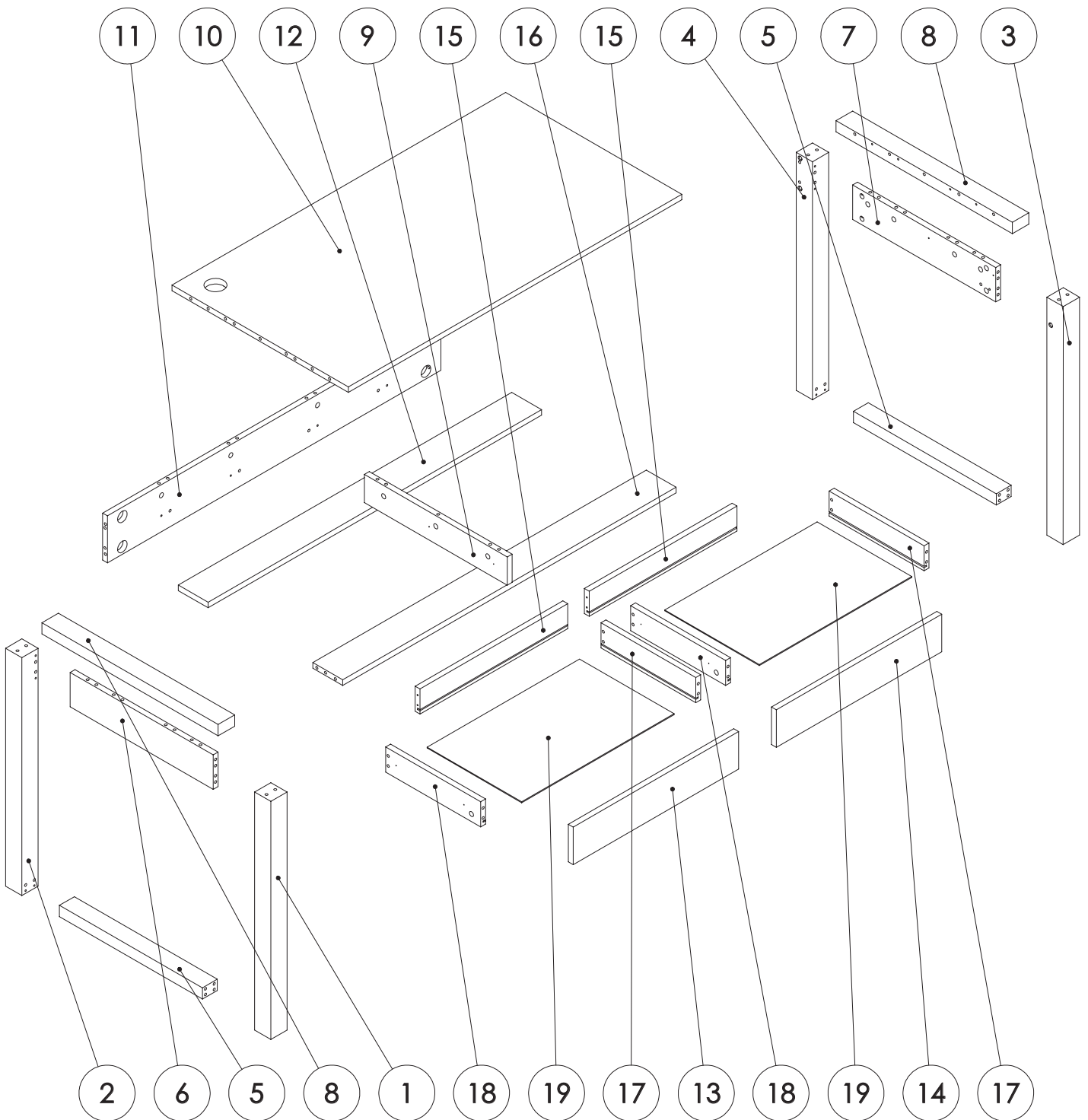




I/O/D

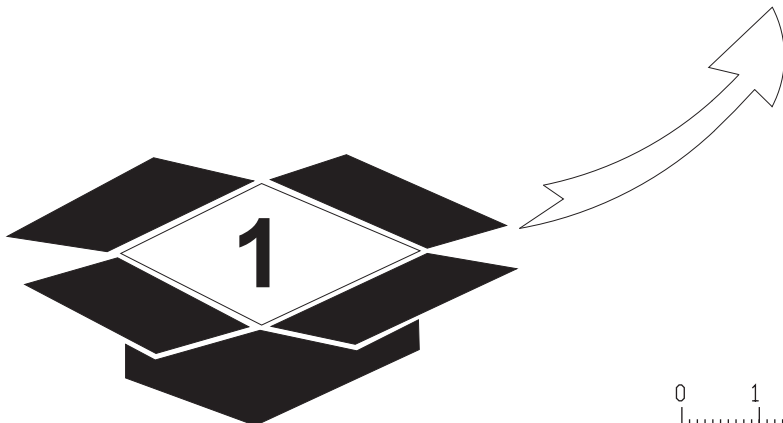
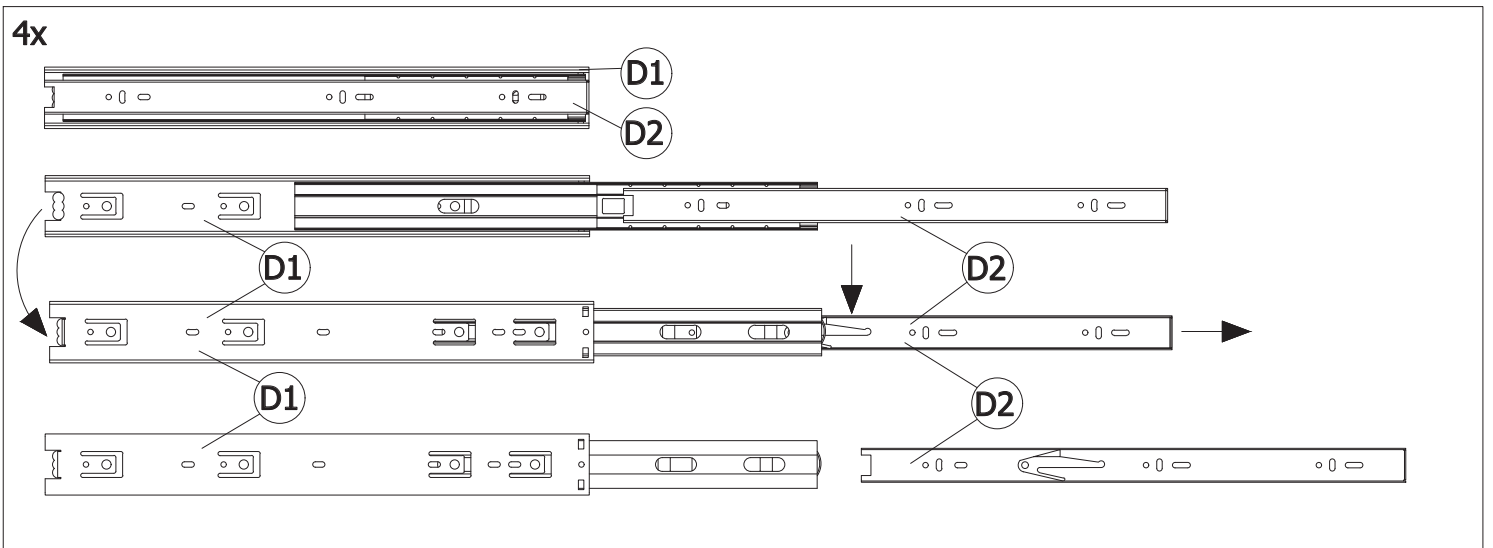
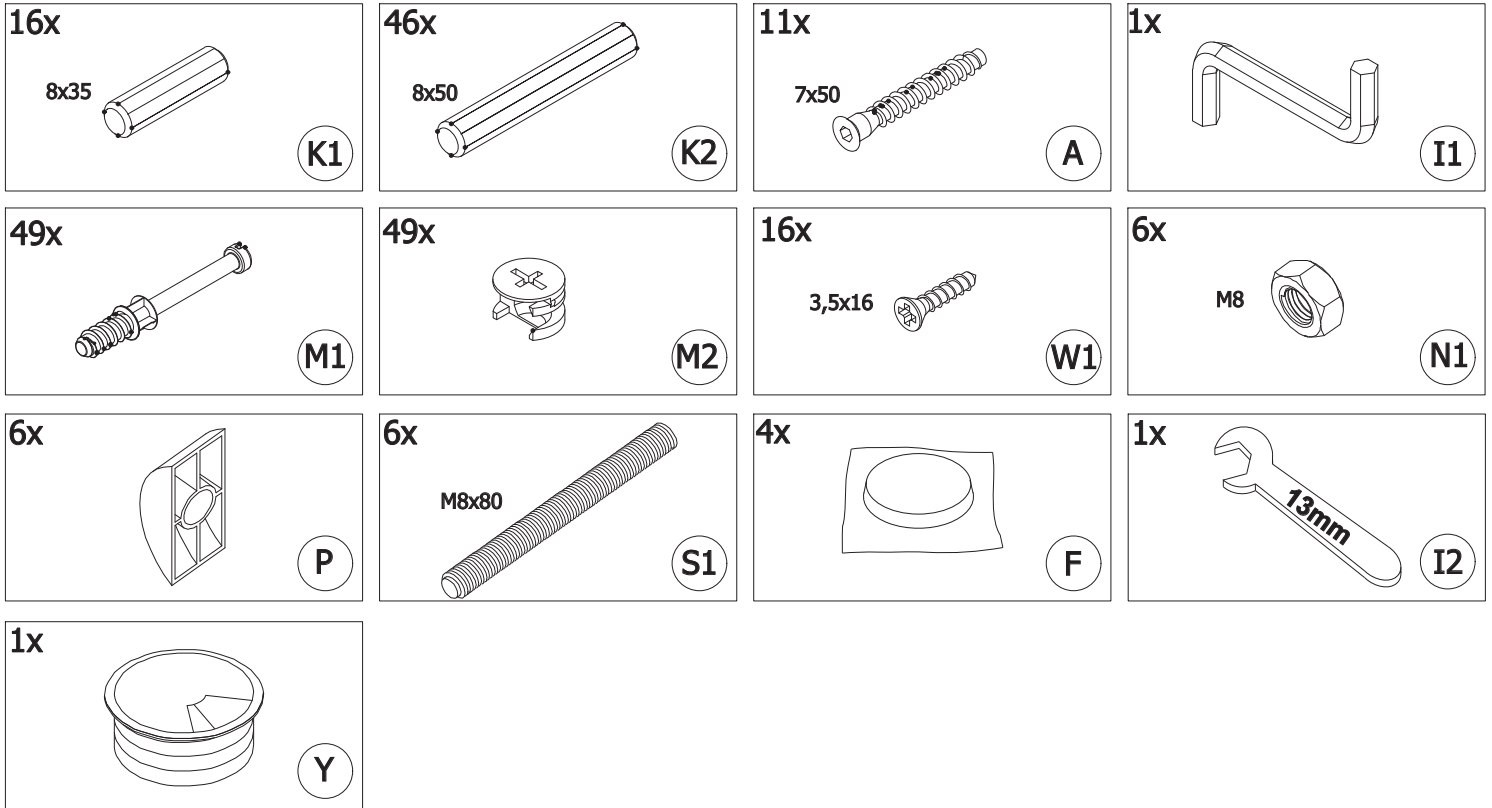




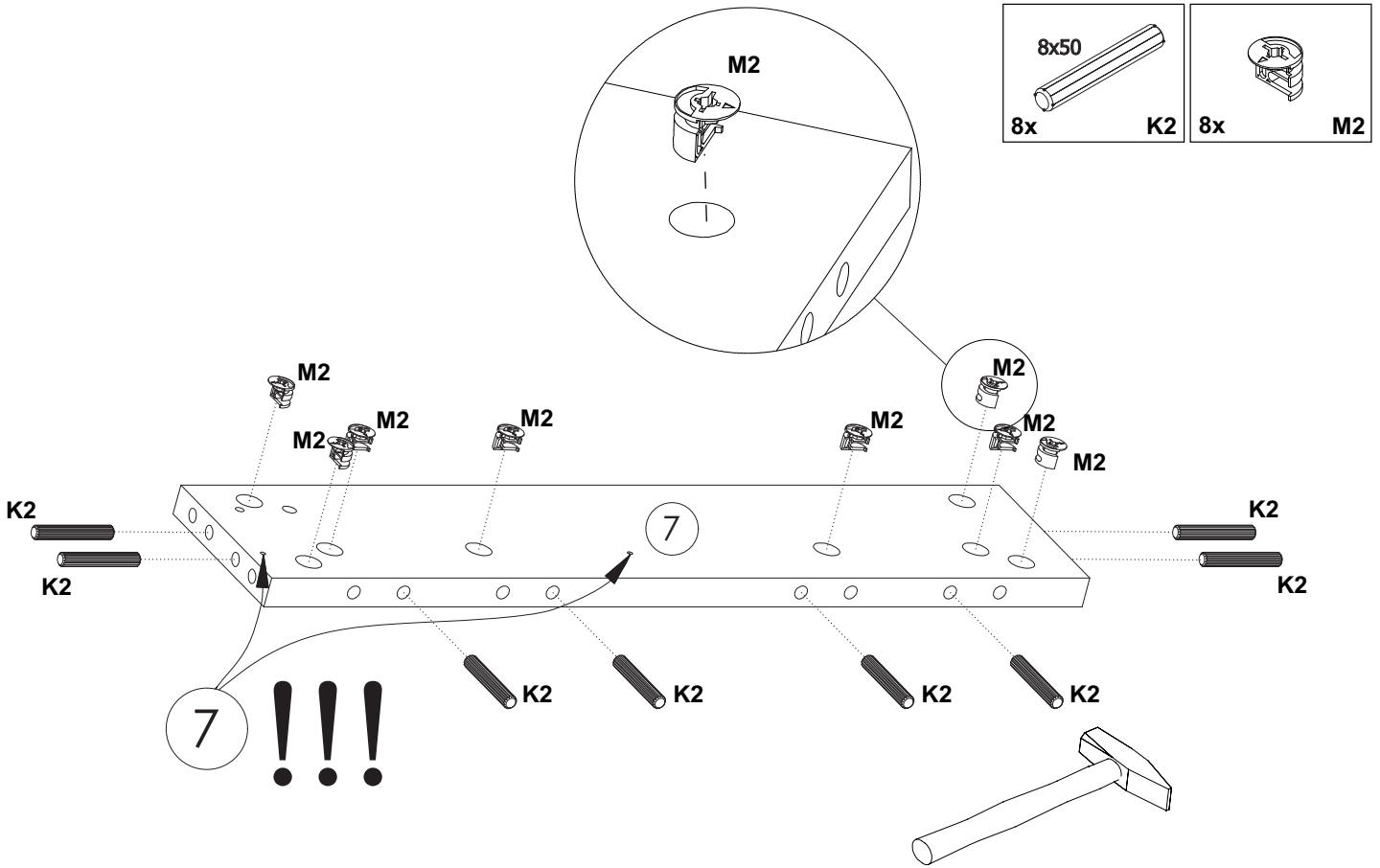


#	L [mm]	H [mm]	T [mm]	PCS.	
1	760	60	60	1	2
2	760	60	60	1	2
3	760	60	60	1	2
4	760	60	60	1	2
5	527	55	36	2	1
6	527	110	18	1	1
7	527	110	18	1	1
8	650	61	36	2	1
9	527	90	18	1	1
10	1230	650	18	1	1

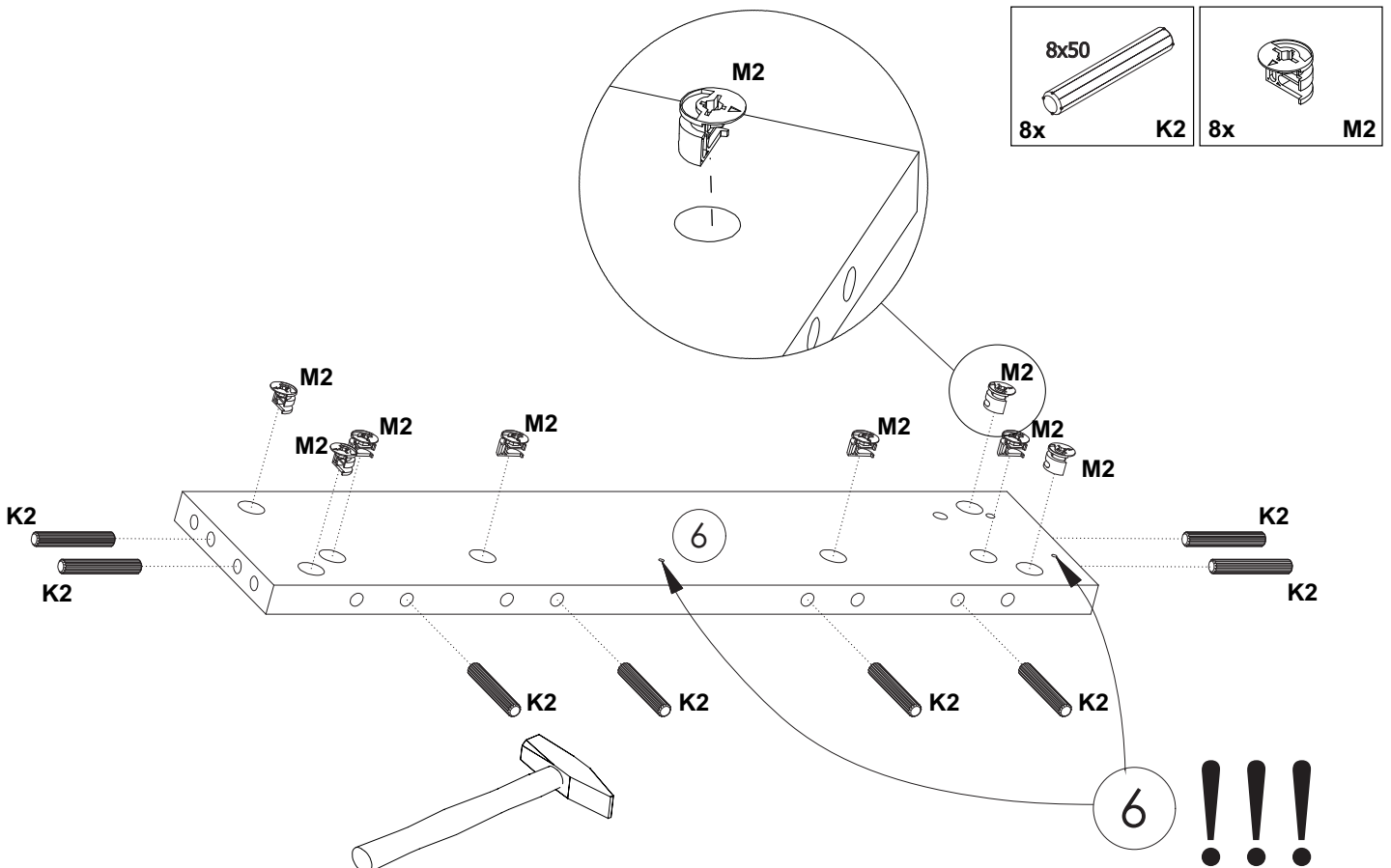
#	L [mm]	H [mm]	T [mm]	PCS.	
11	1230	150	18	1	1
12	1228	110	18	1	1
13	610	120	18	1	1
14	610	120	18	1	1
15	545	80	18	2	1
16	1230	110	18	1	1
17	350	80	18	2	1
18	350	80	18	2	1
19	562	349	3	2	1



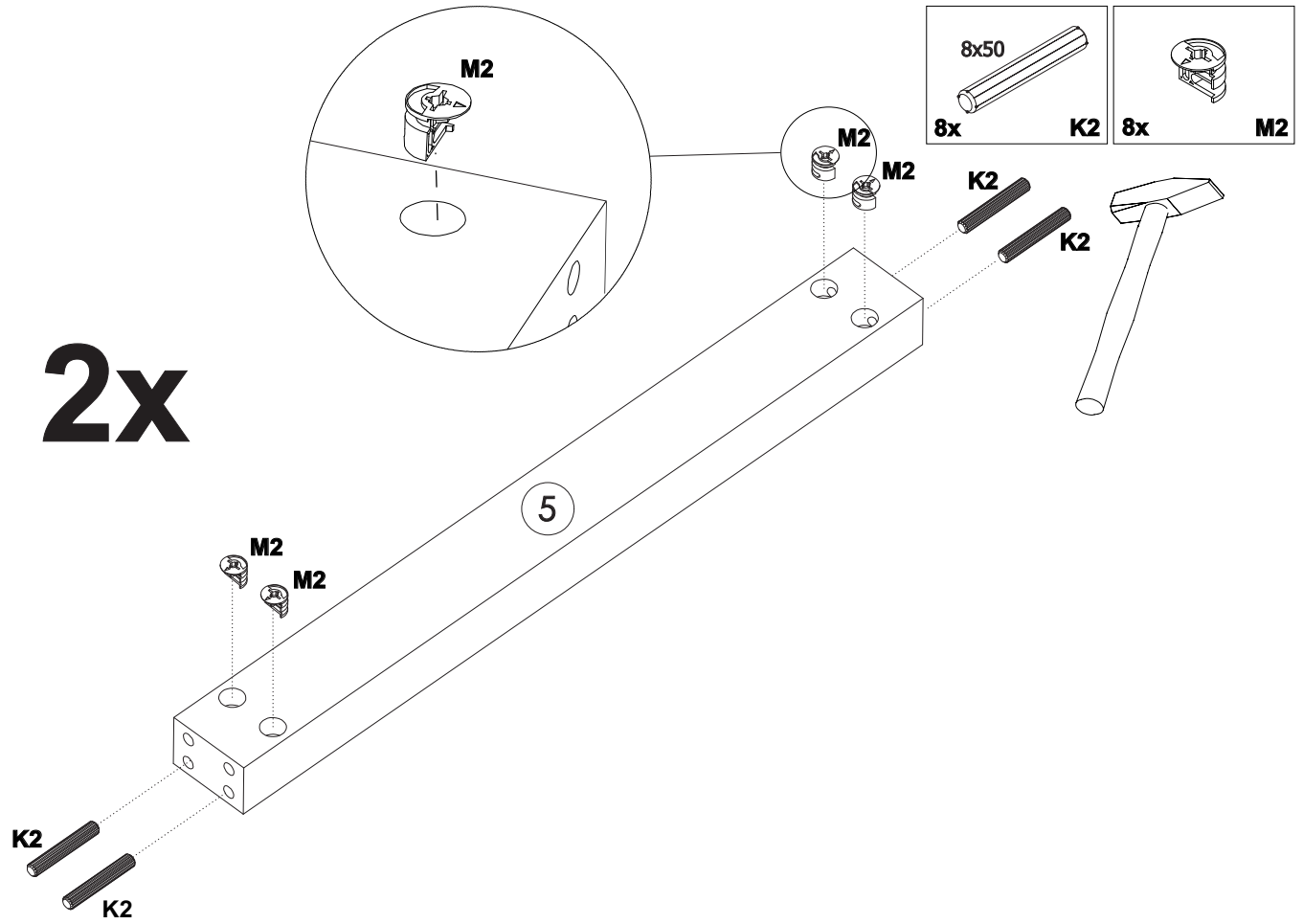
1



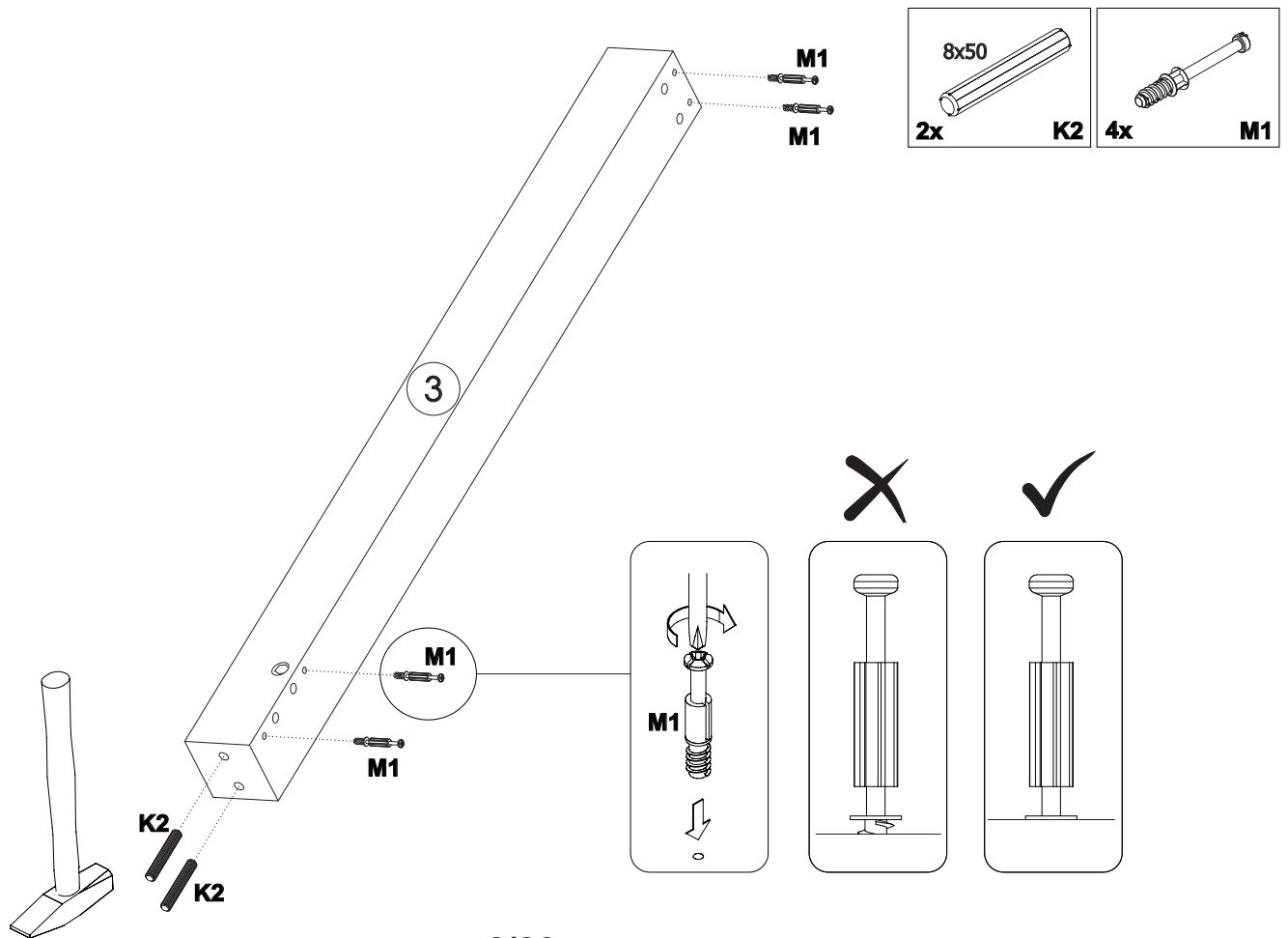
2



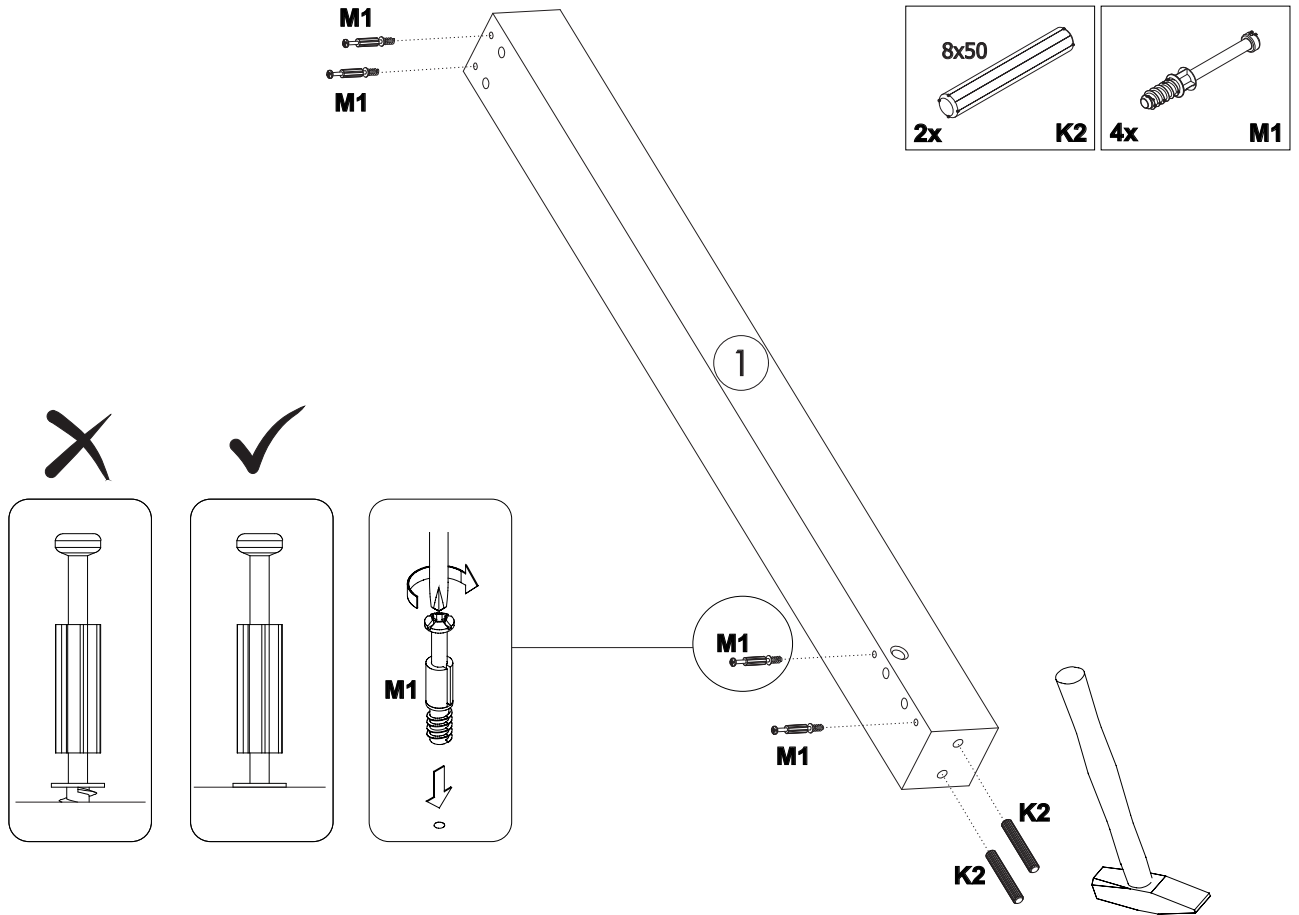
3



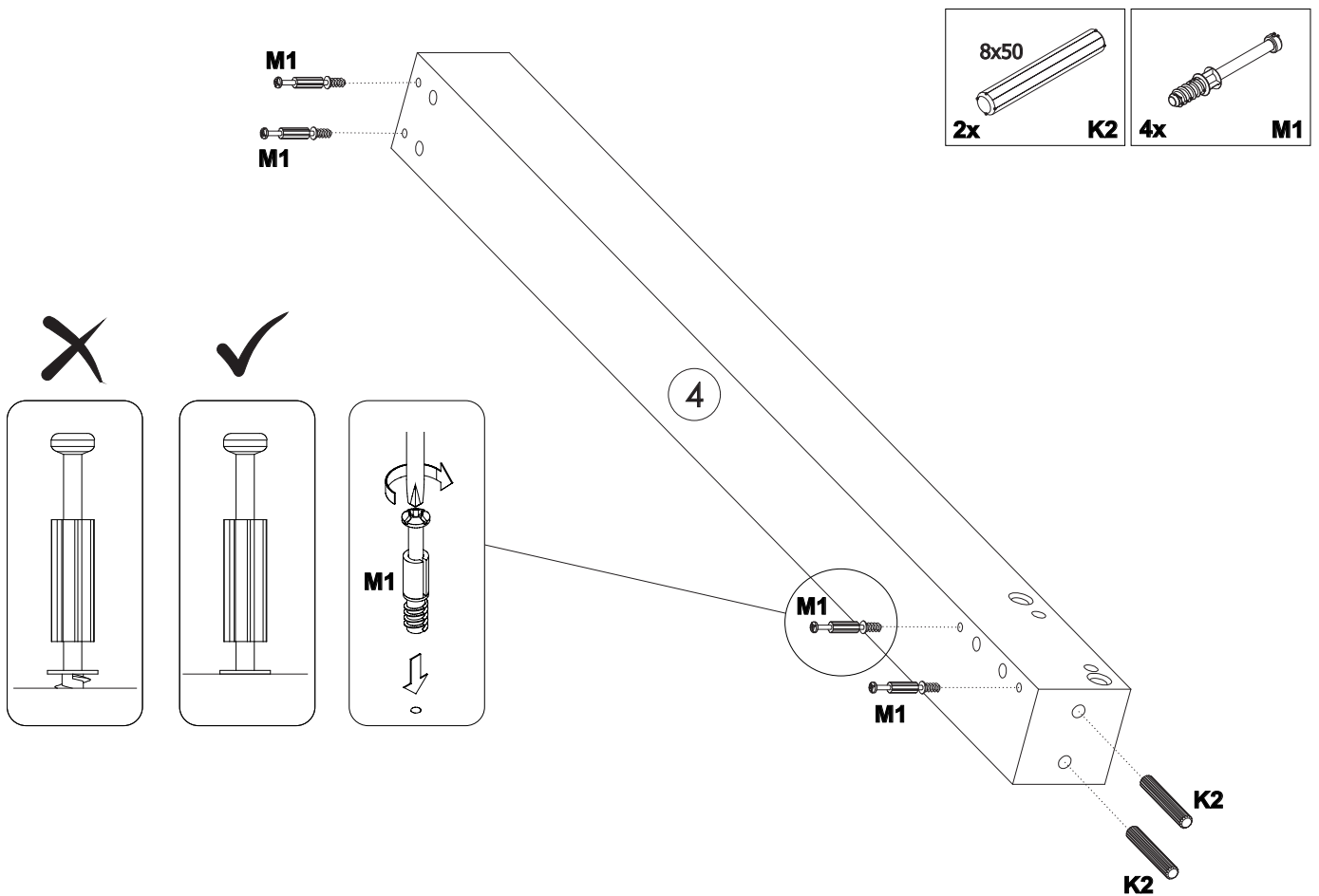
4



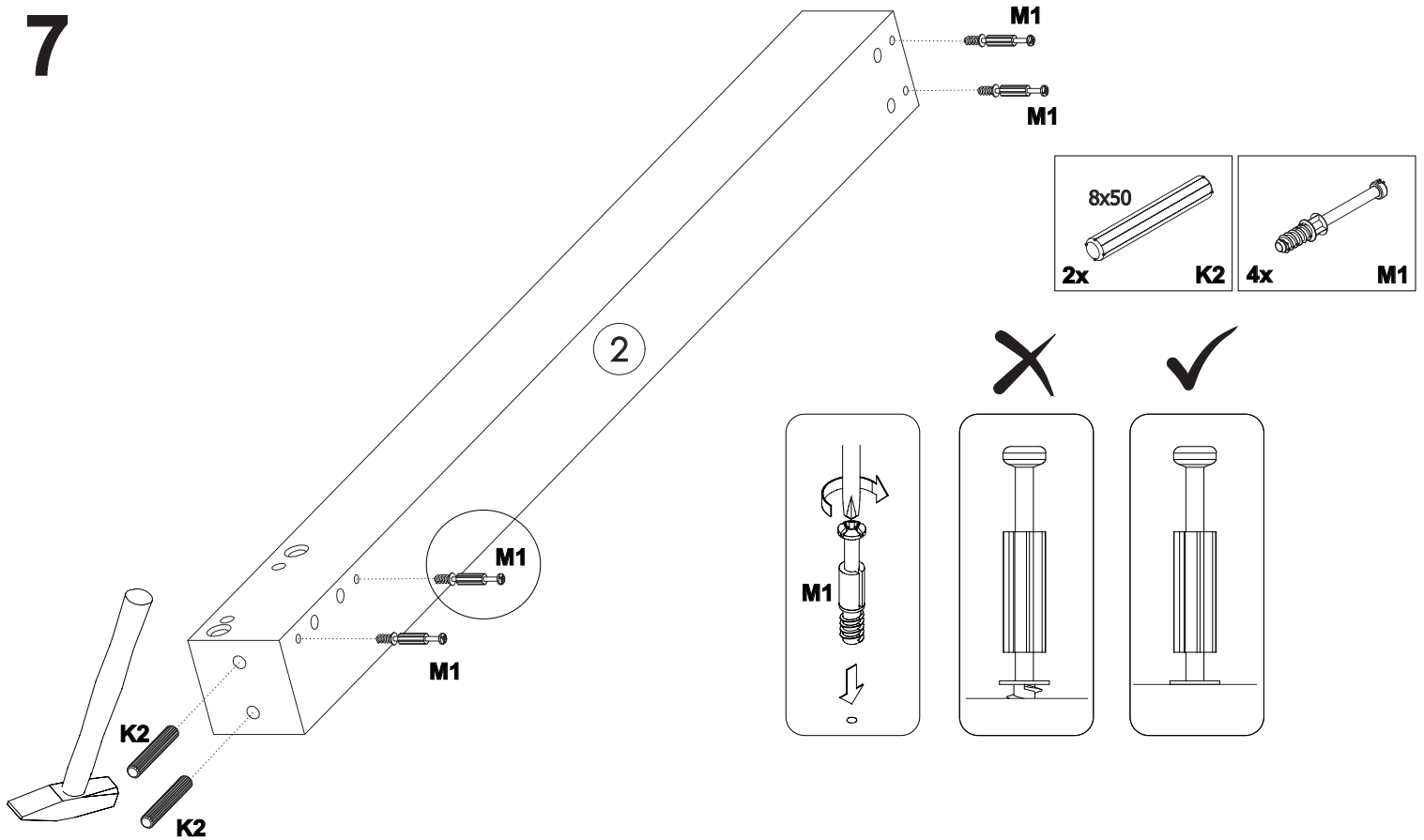
5



6

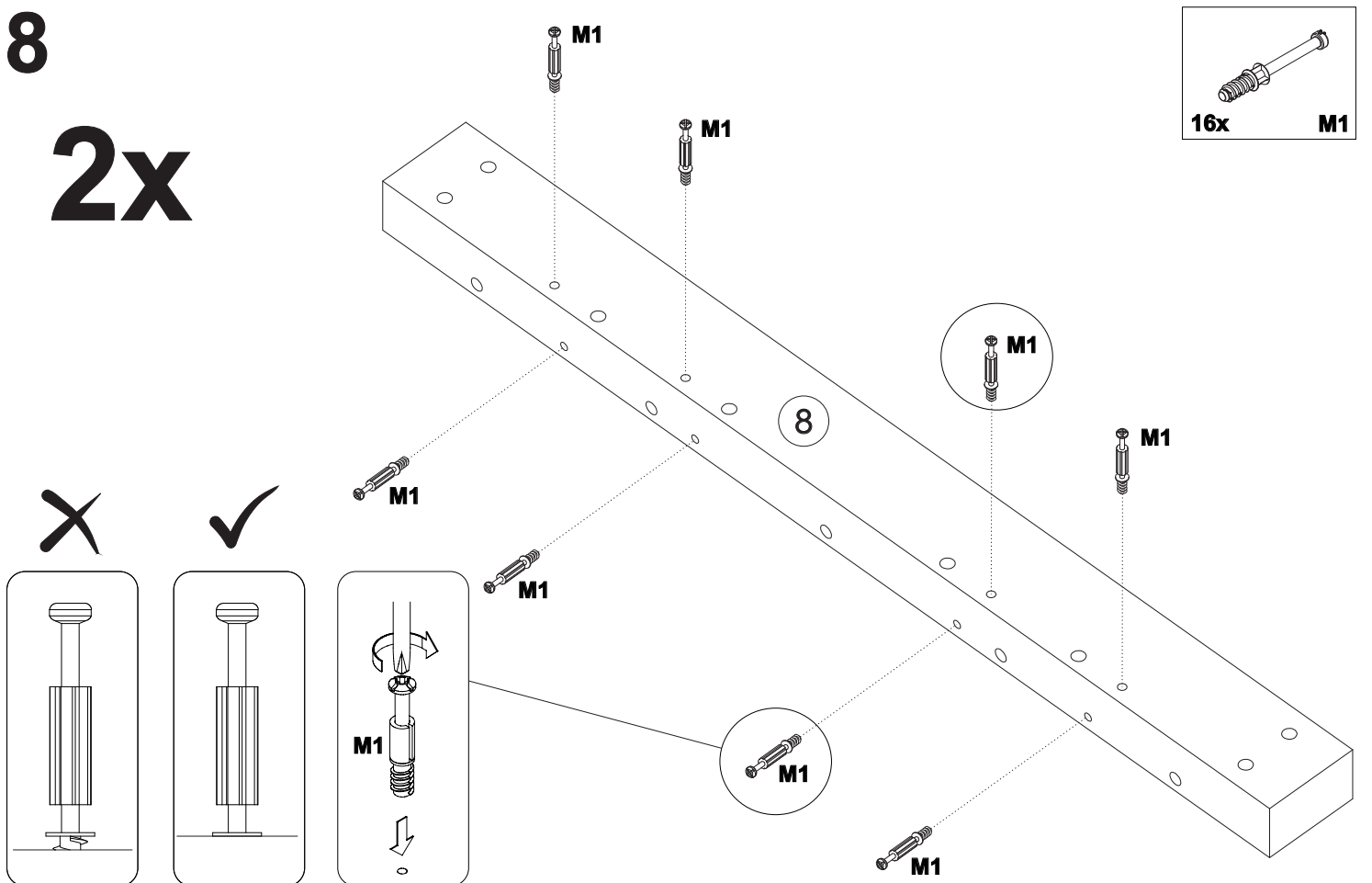


7

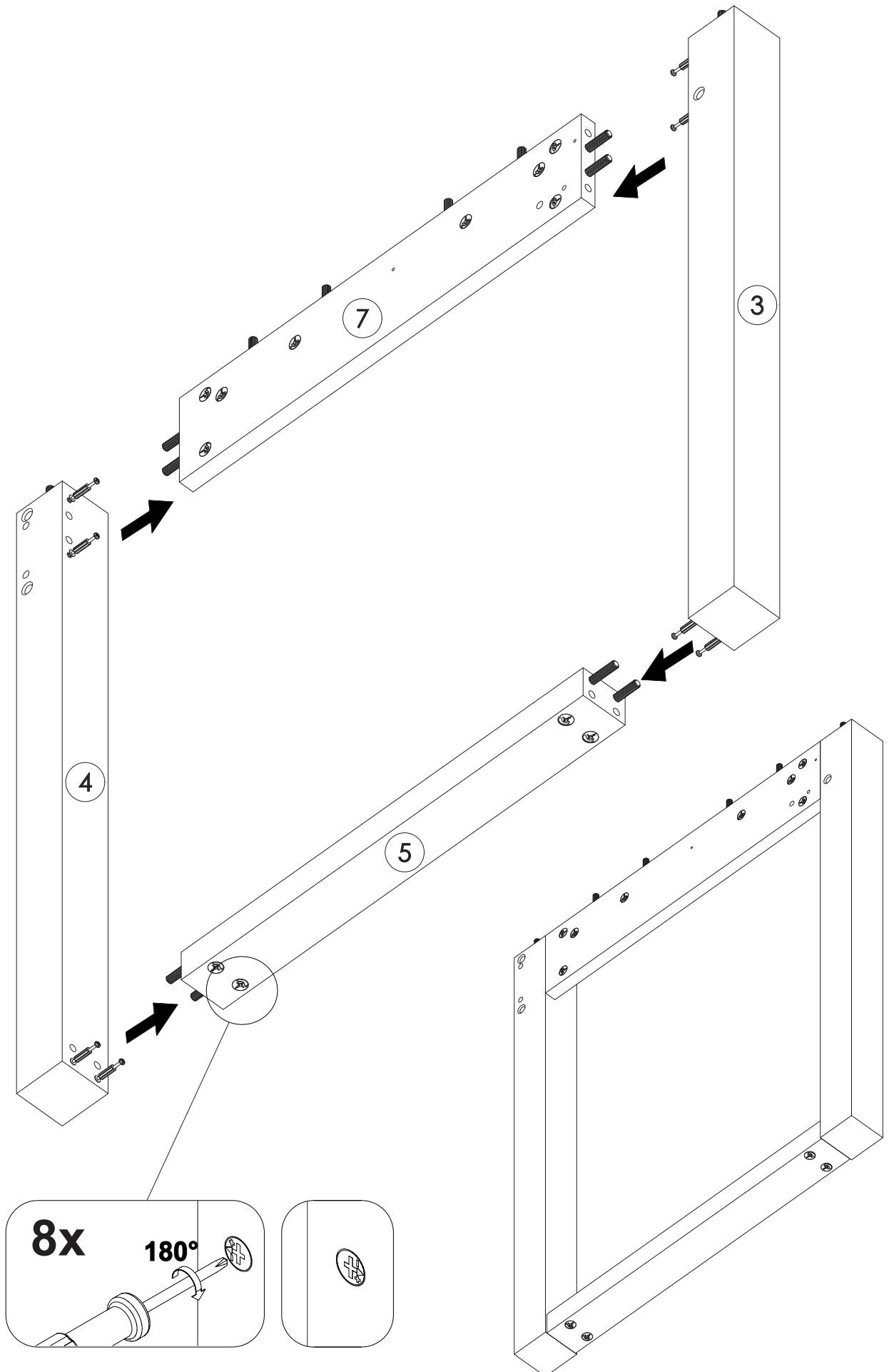


8

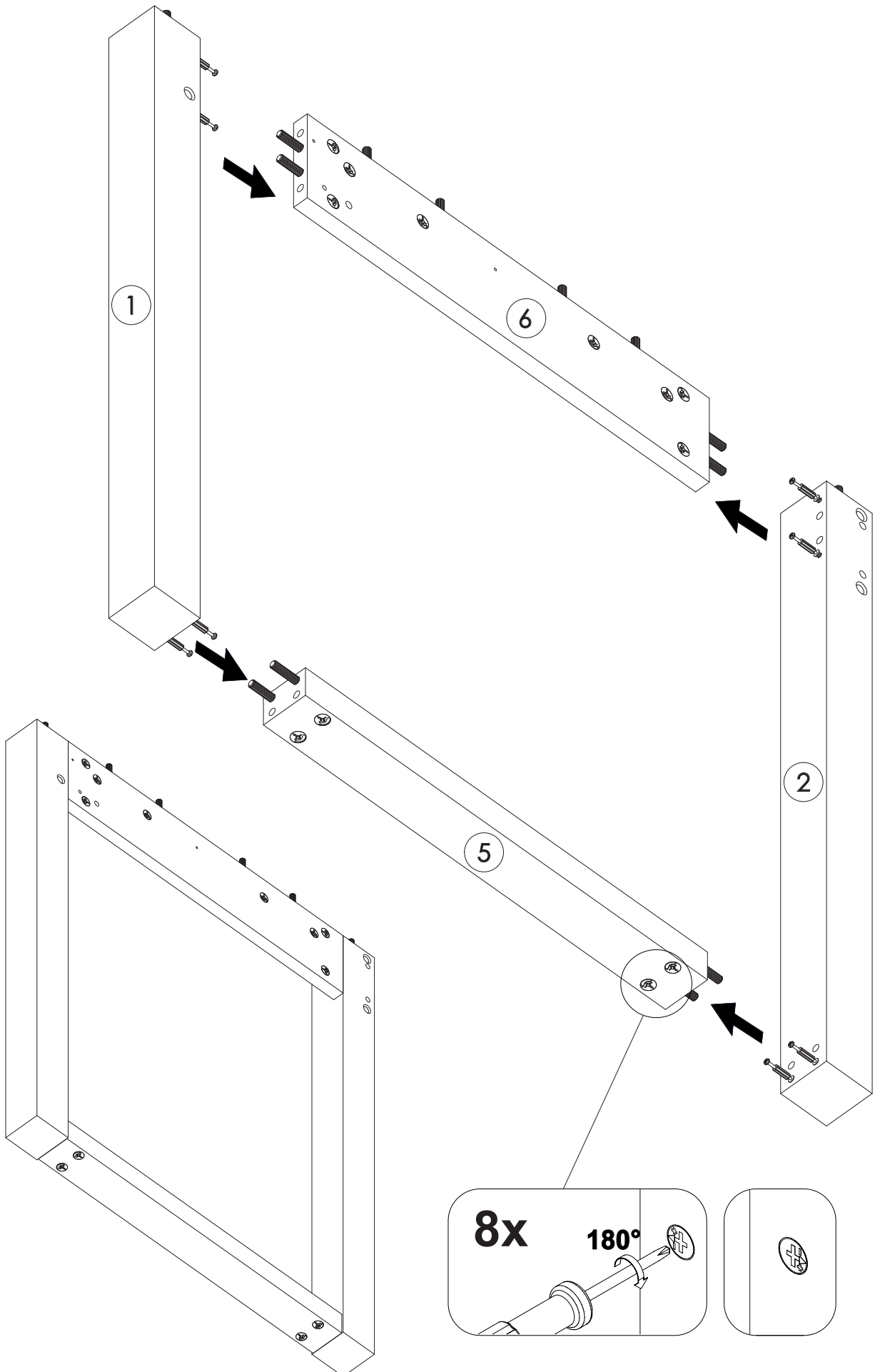
2x



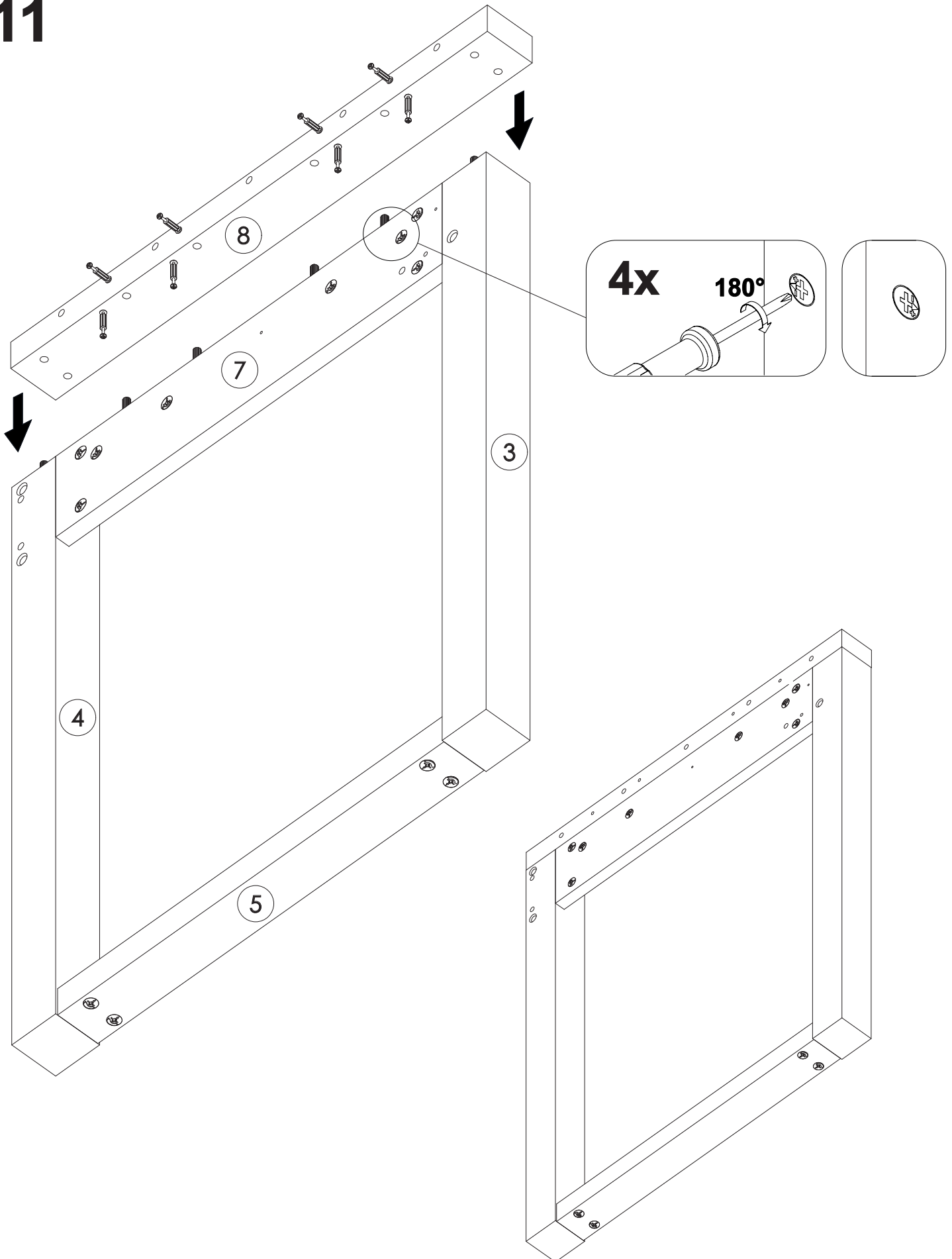
9



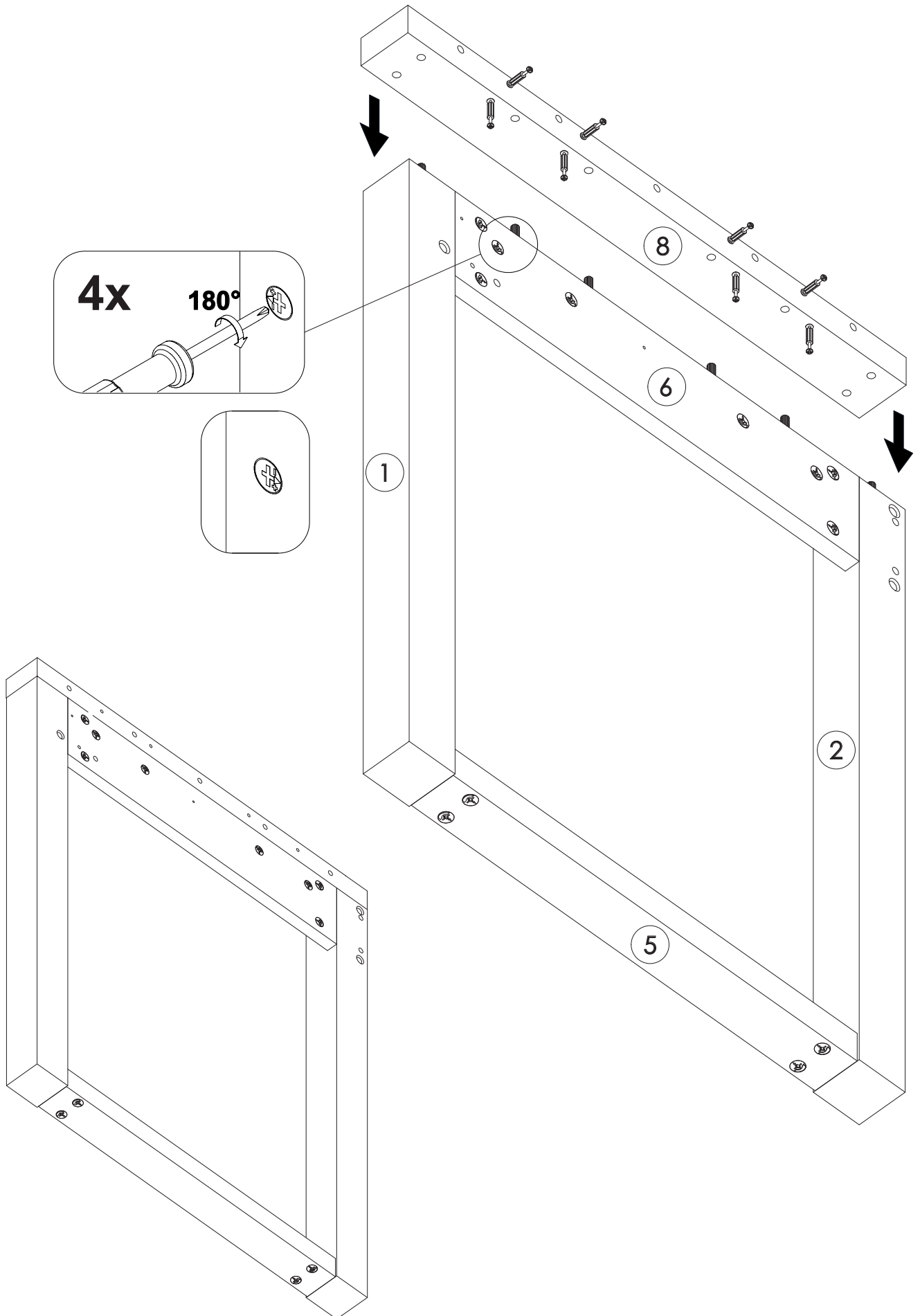
10



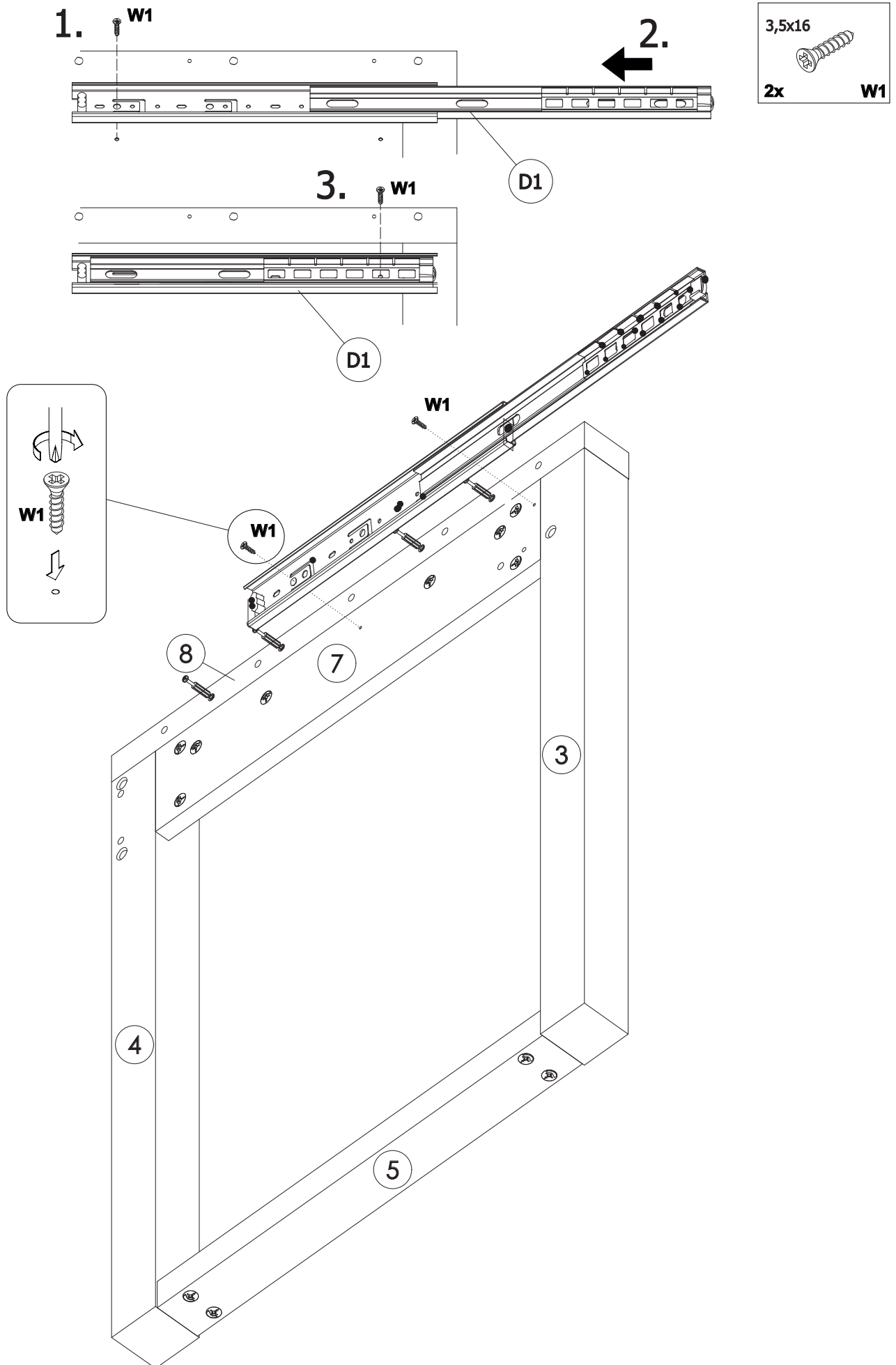
11



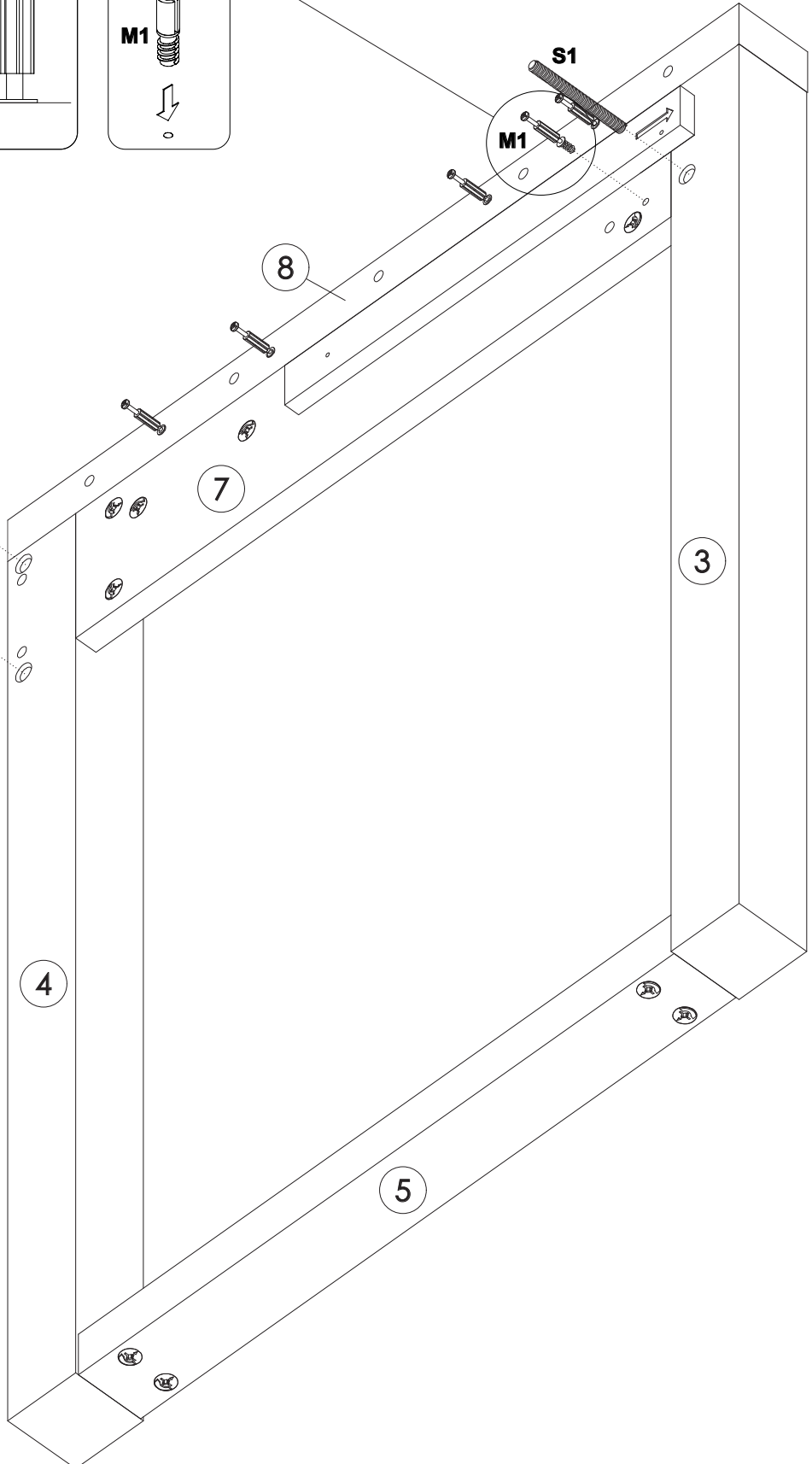
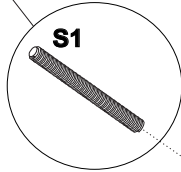
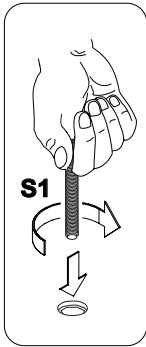
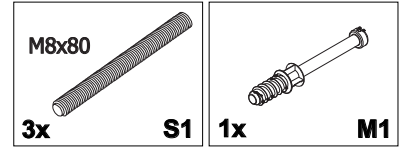
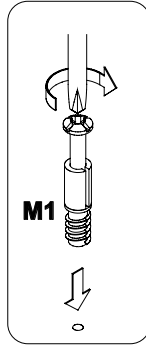
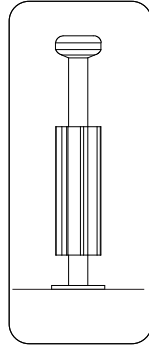
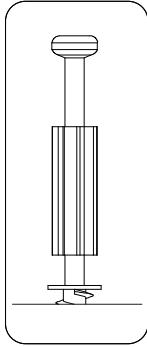
12



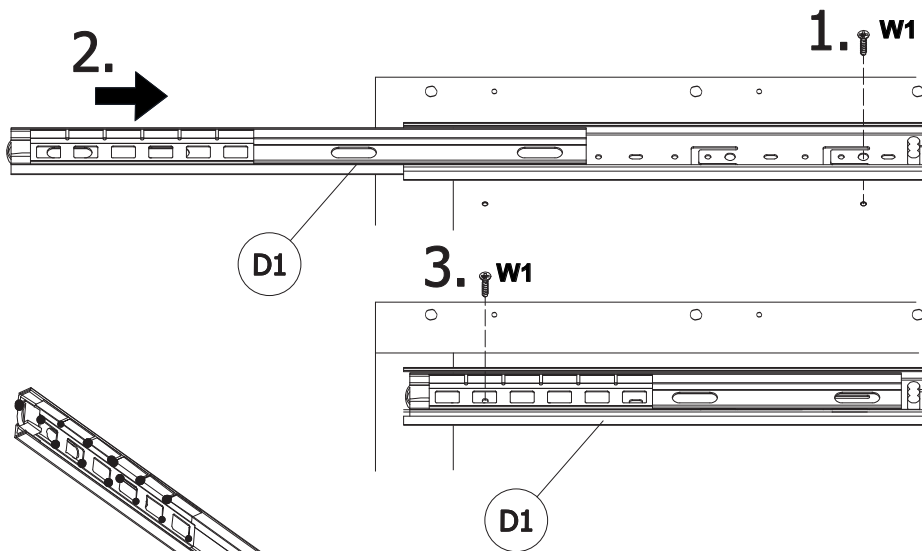
13



14



15

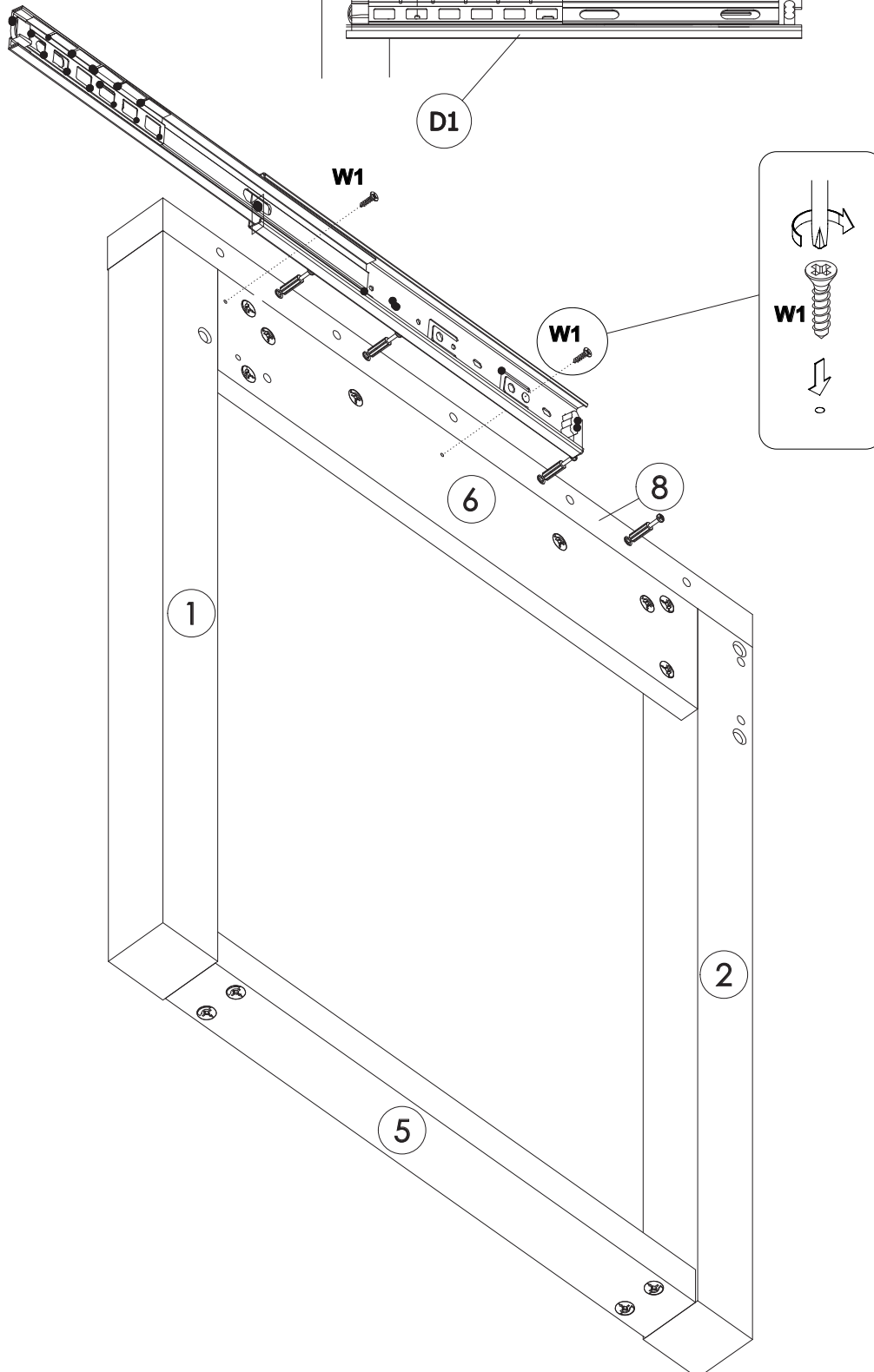


3,5x16

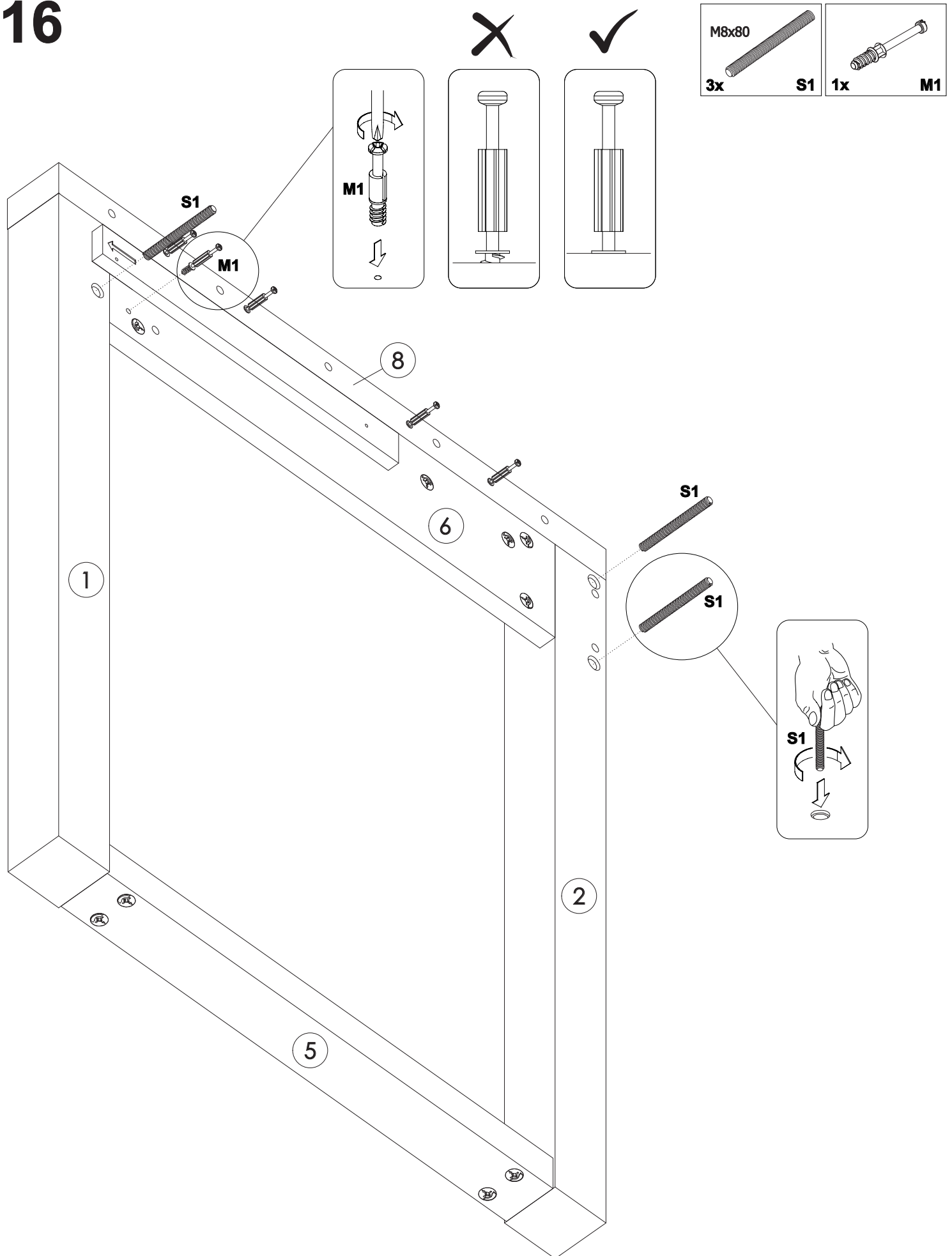


2x

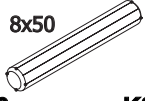
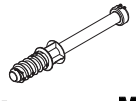

W1

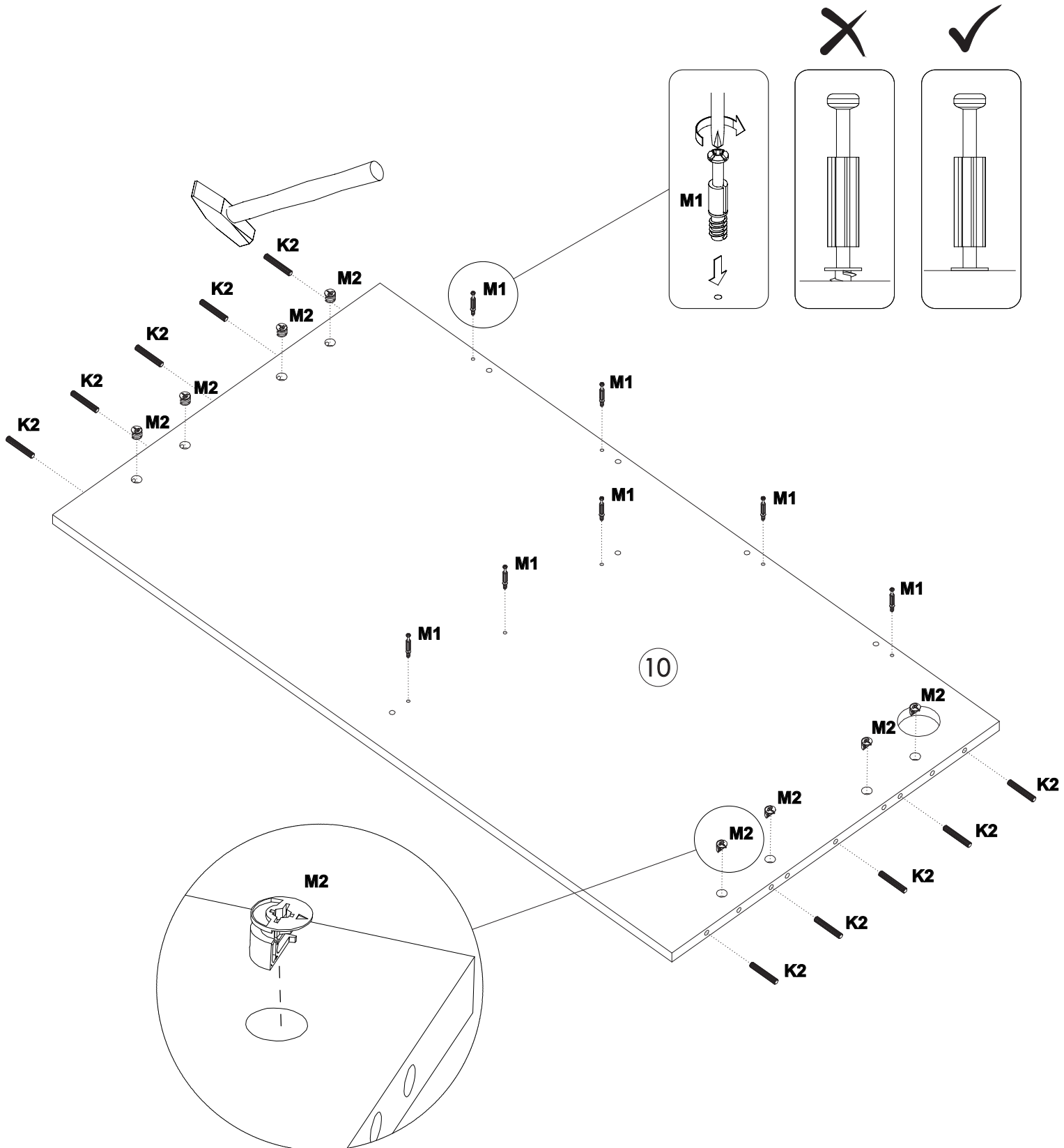


16

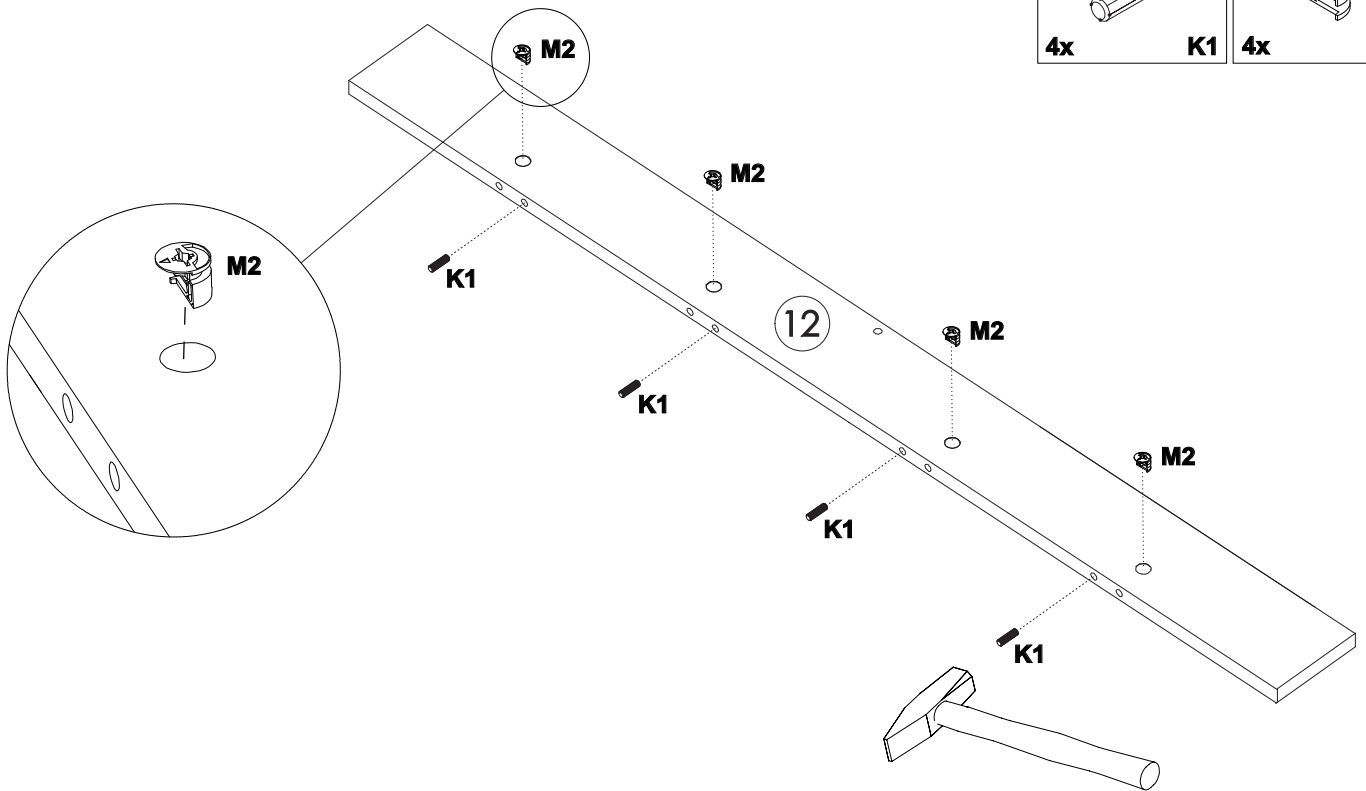
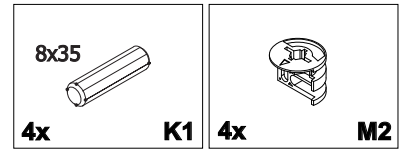


17

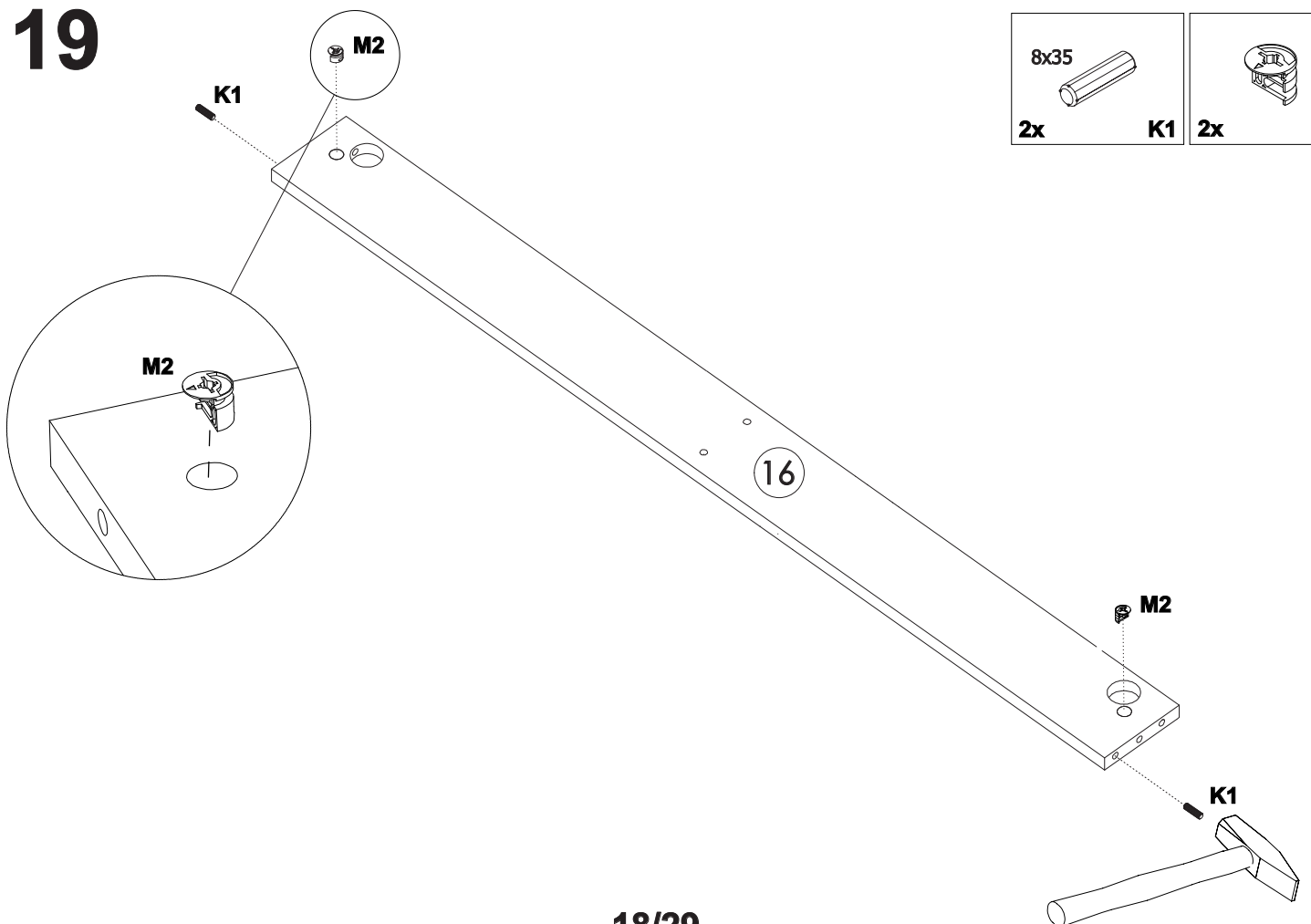
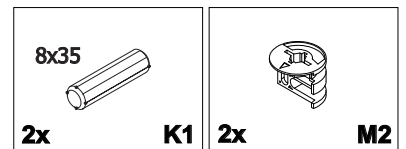
 8x50 10x	 M1 7x	 M2 8x
---	---	---



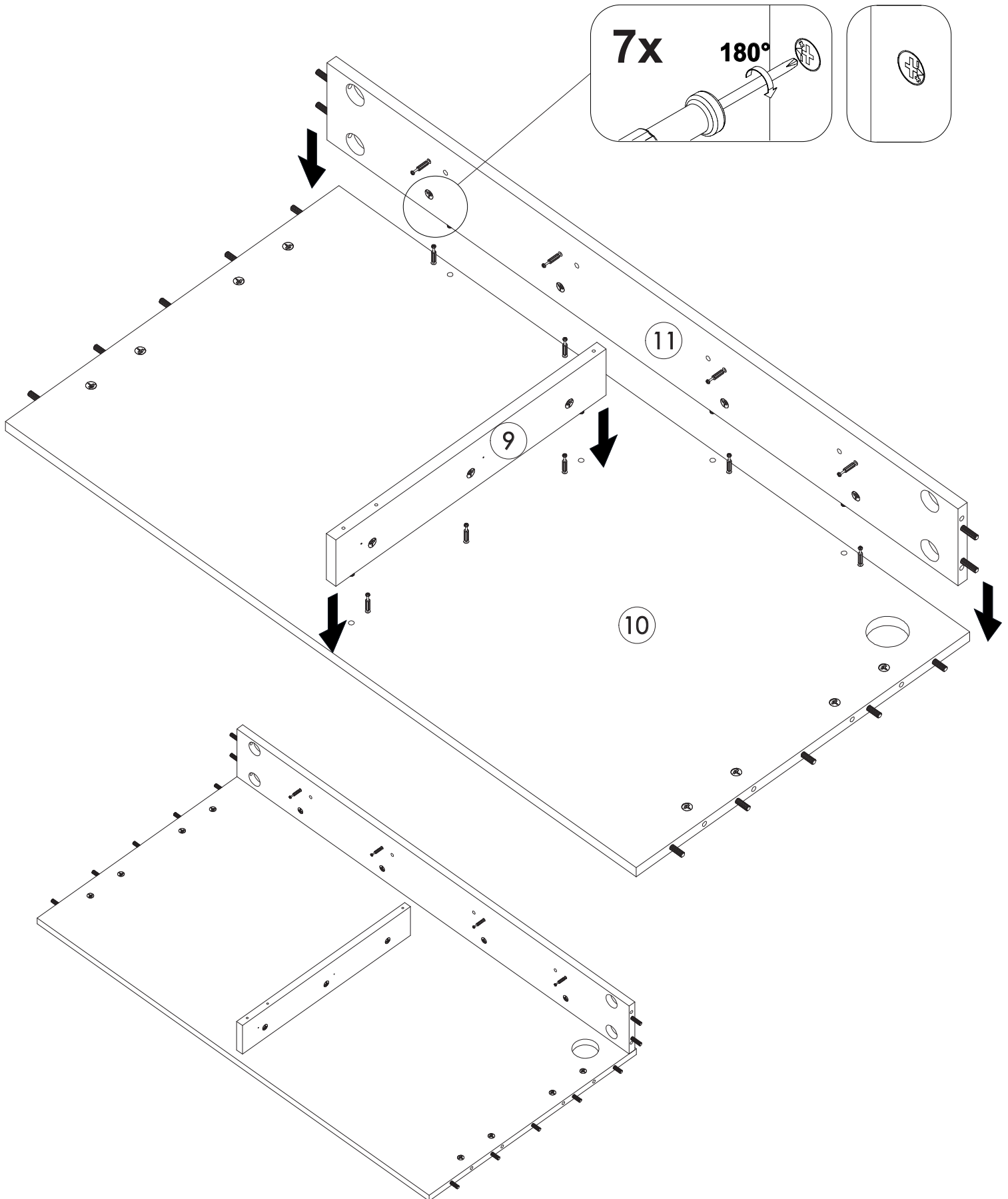
18



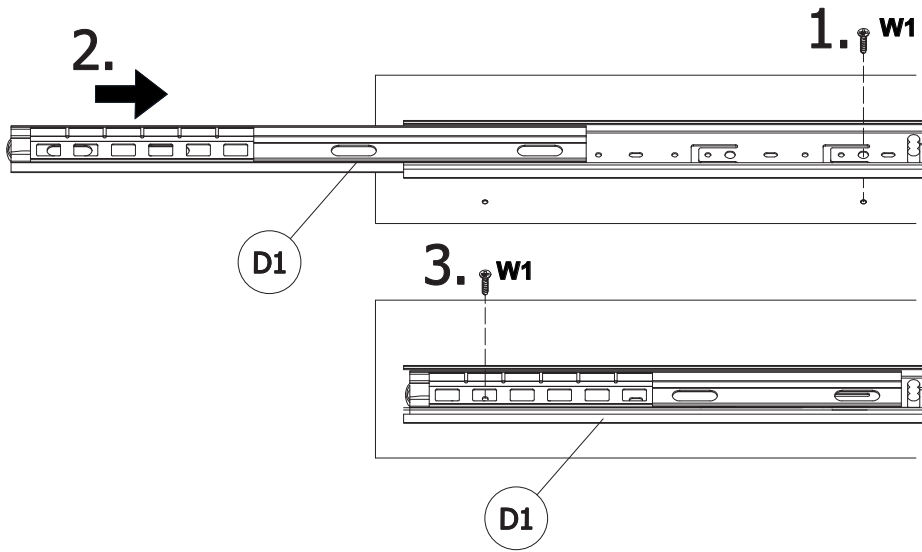
19



22

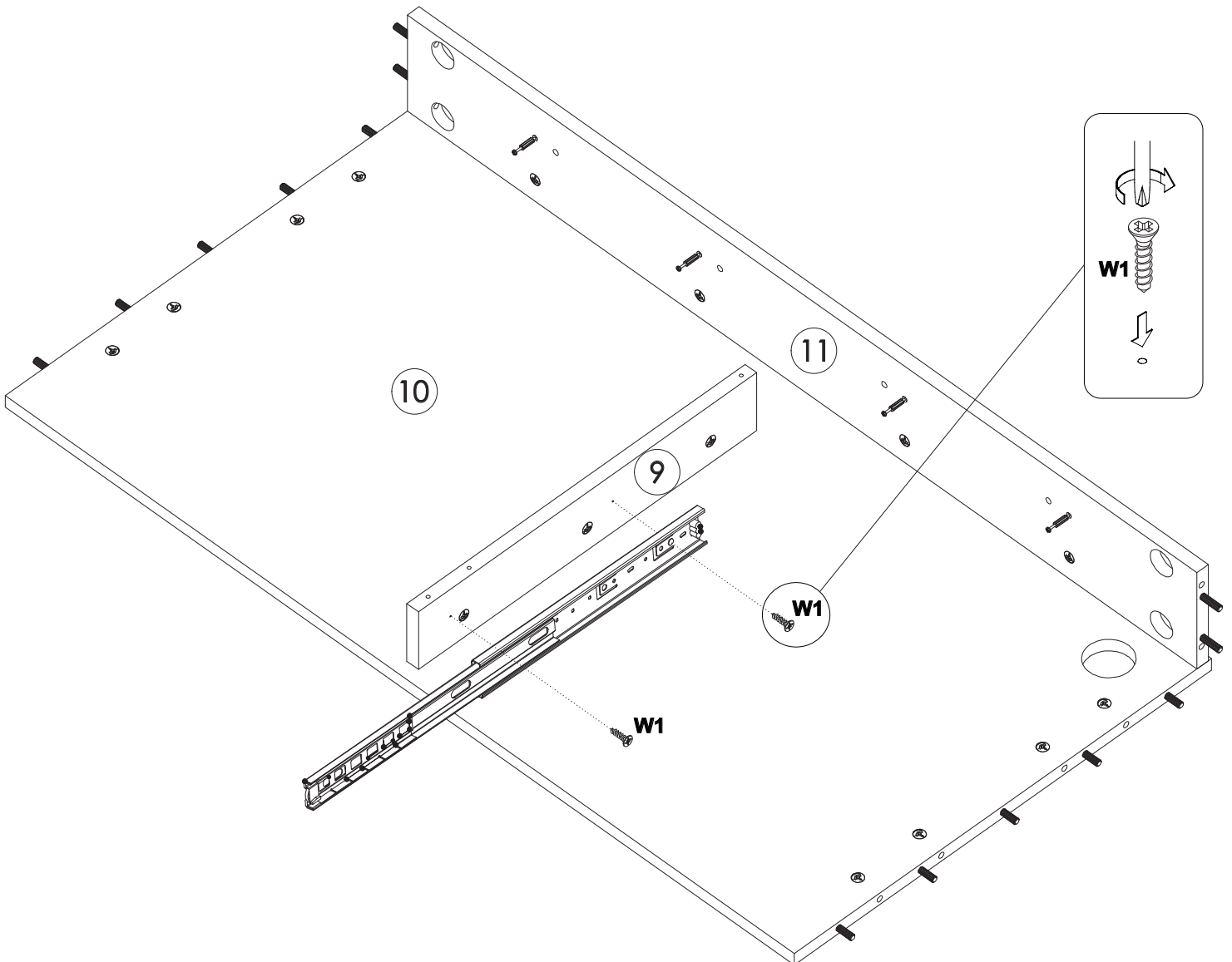


23

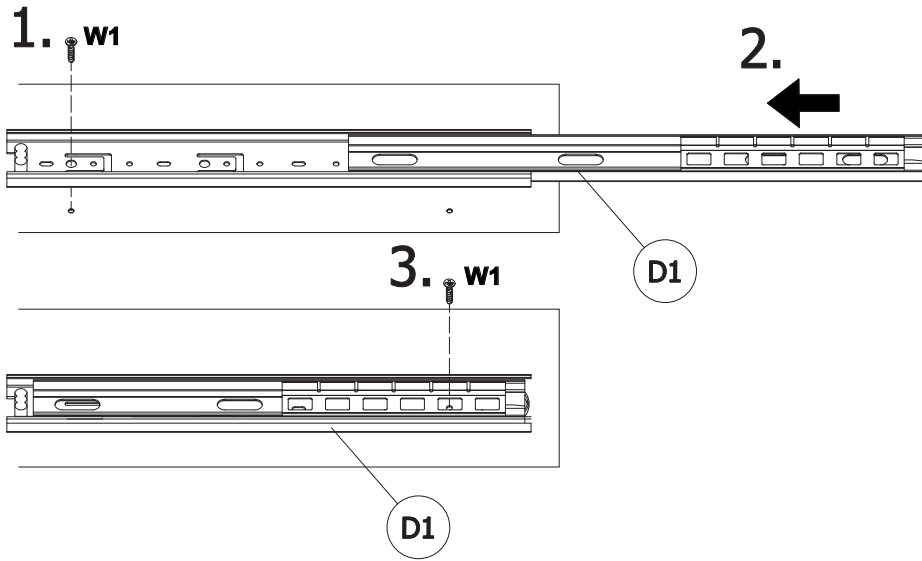


3,5x16

2x W1



24

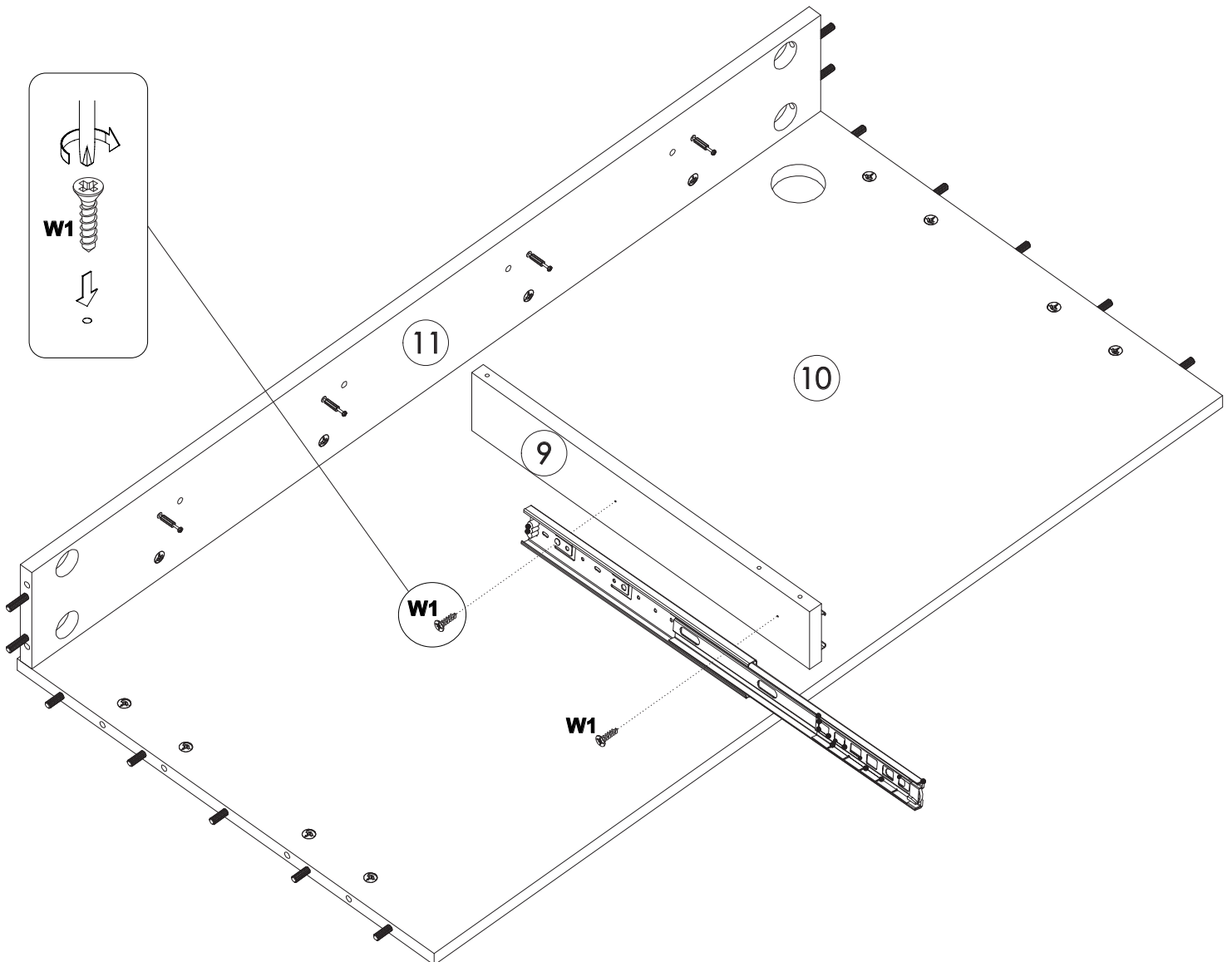


3,5x16

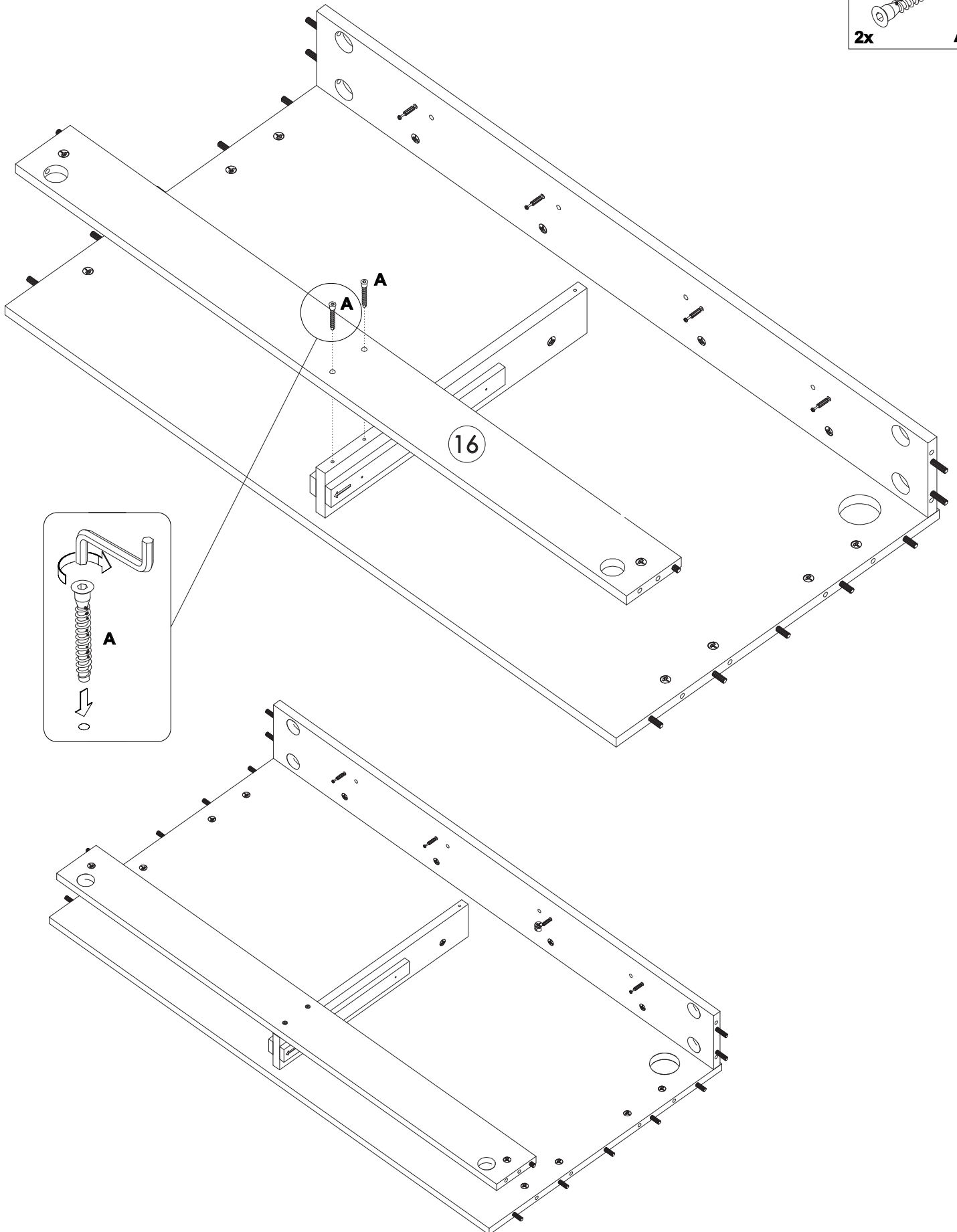
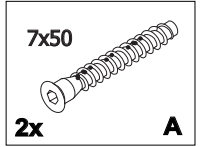


2x

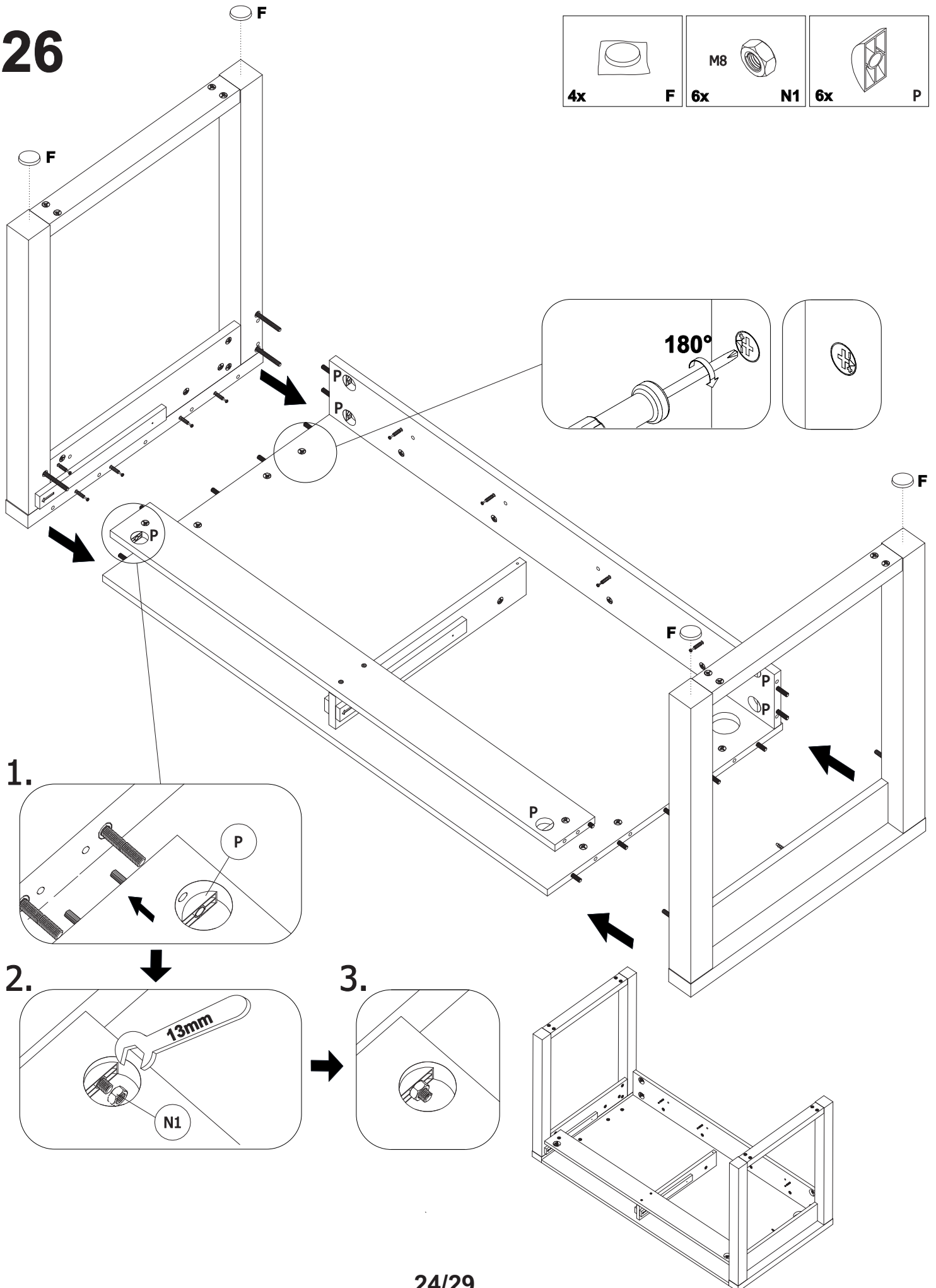
W1



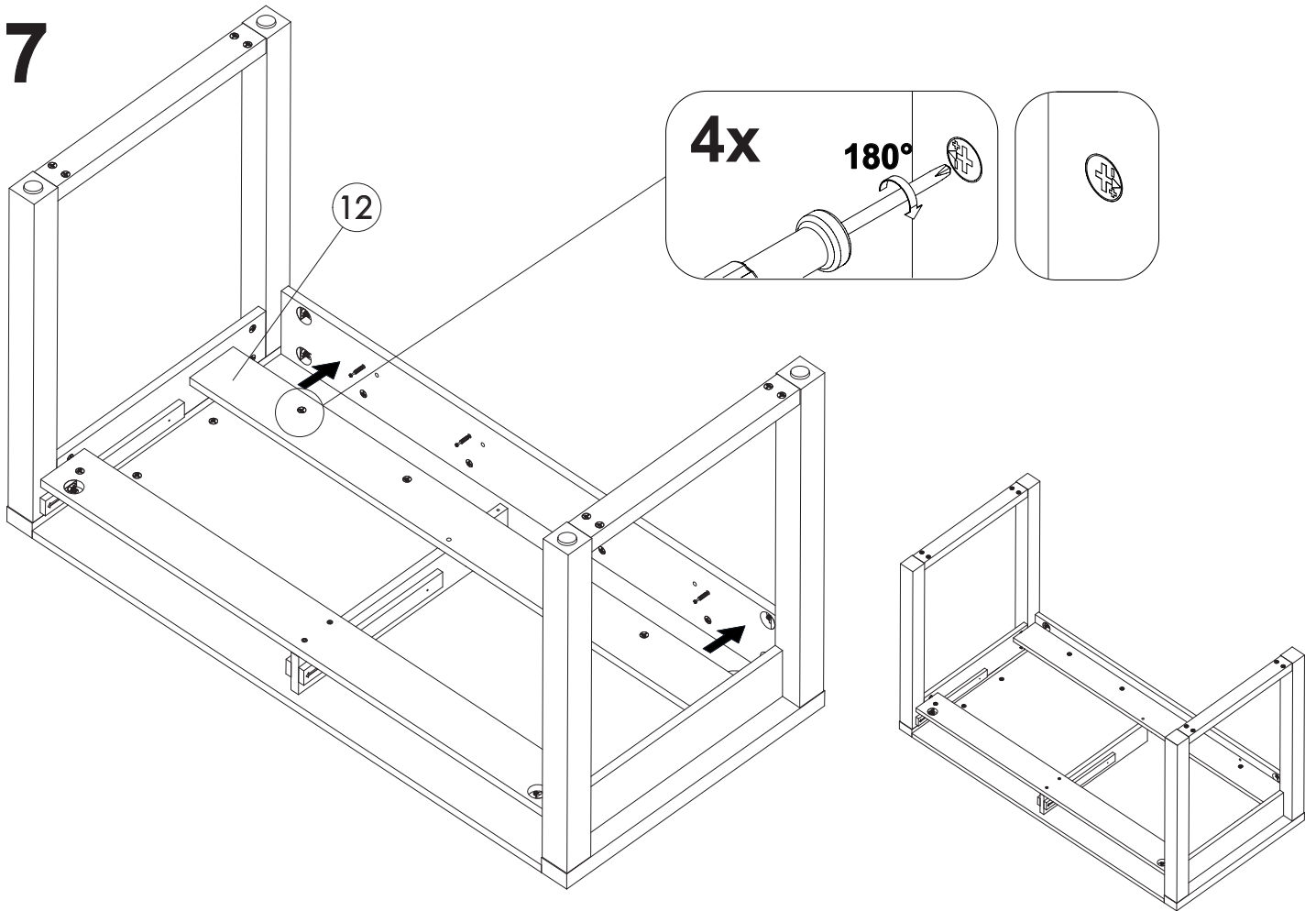
25



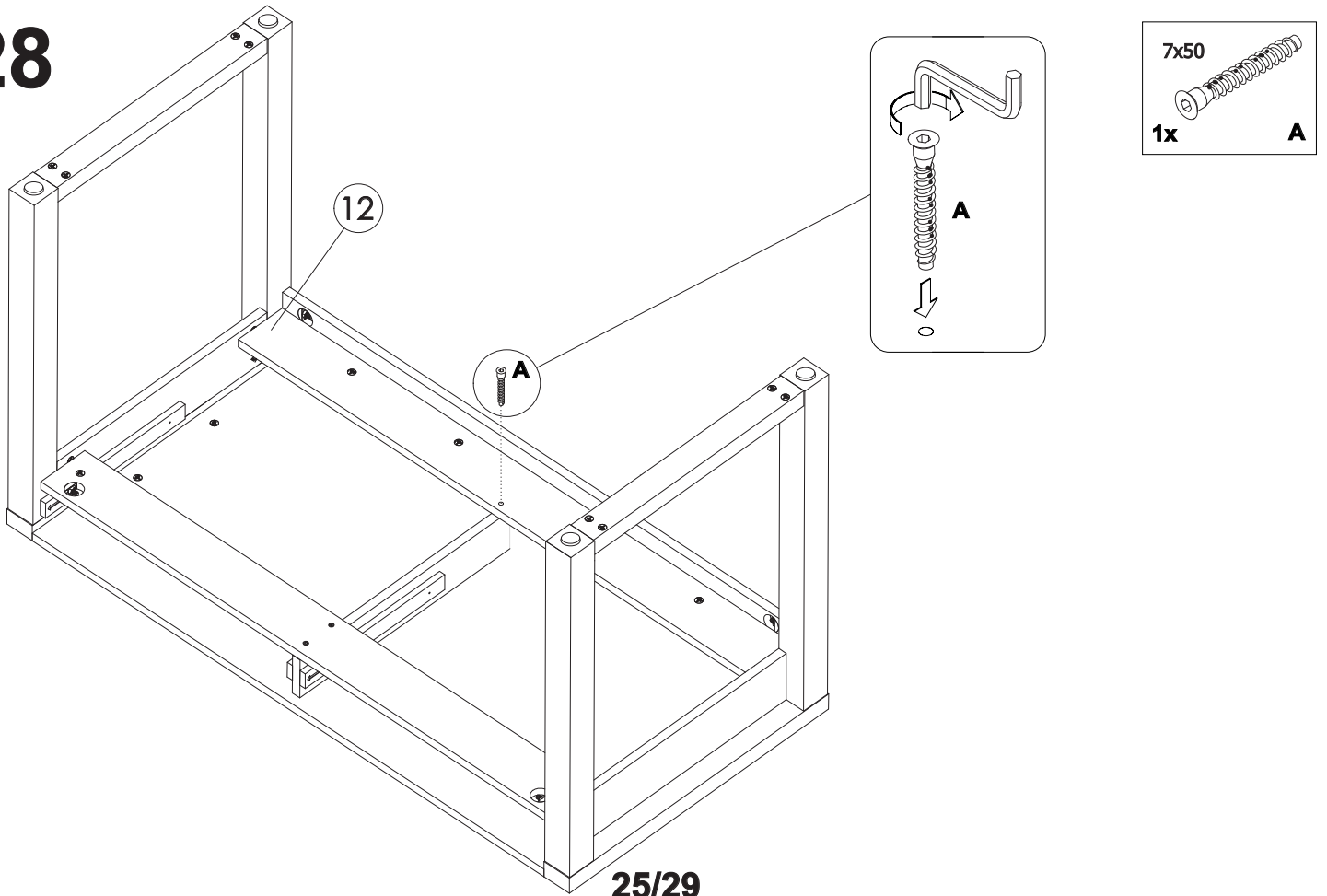
26



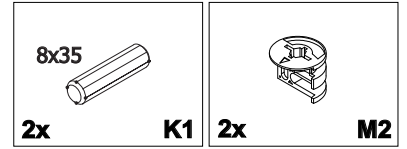
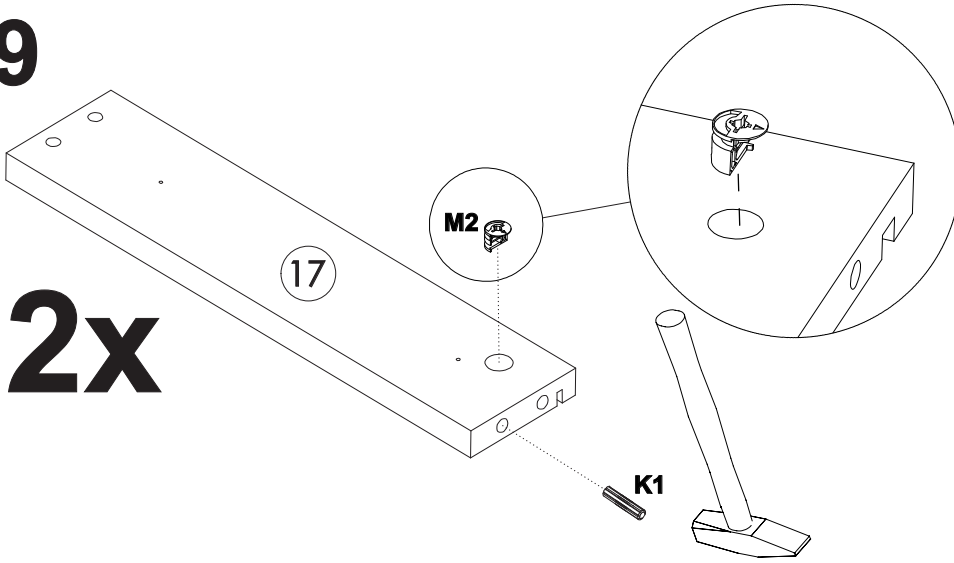
27



28

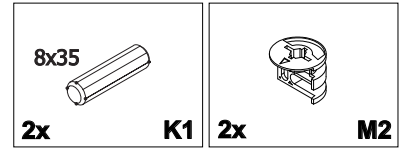
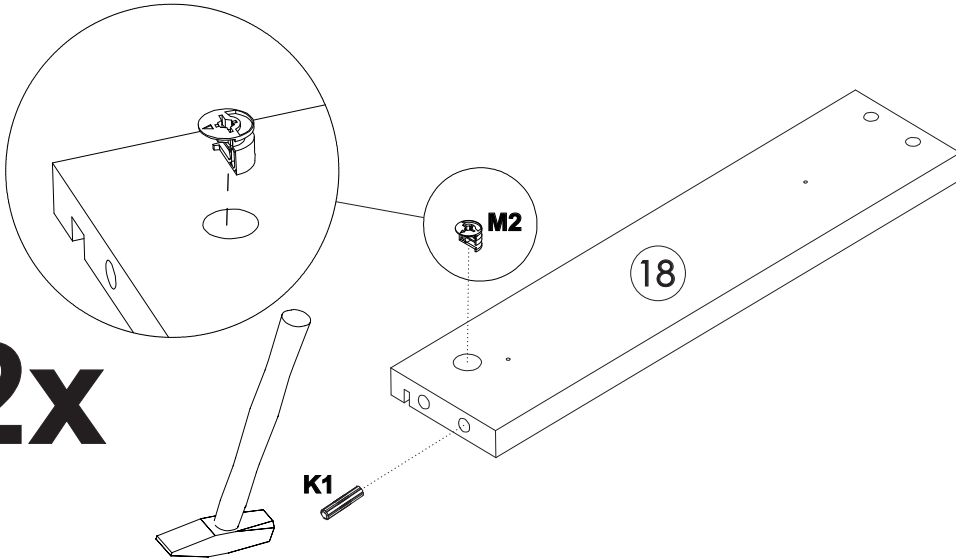


29



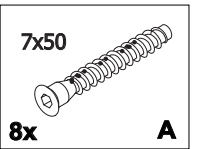
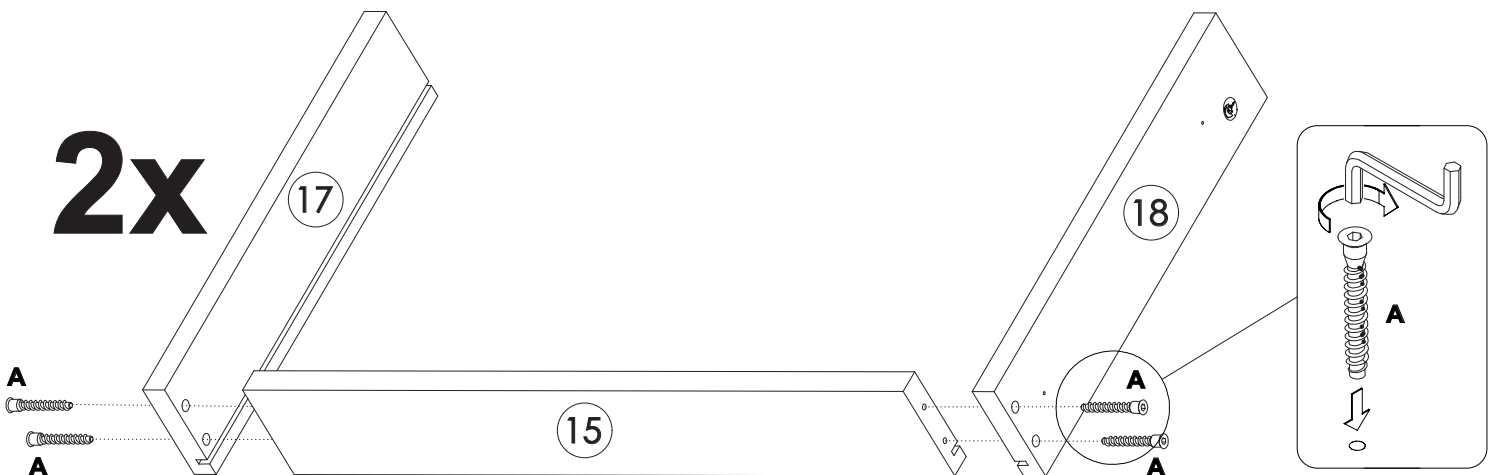
2x

30



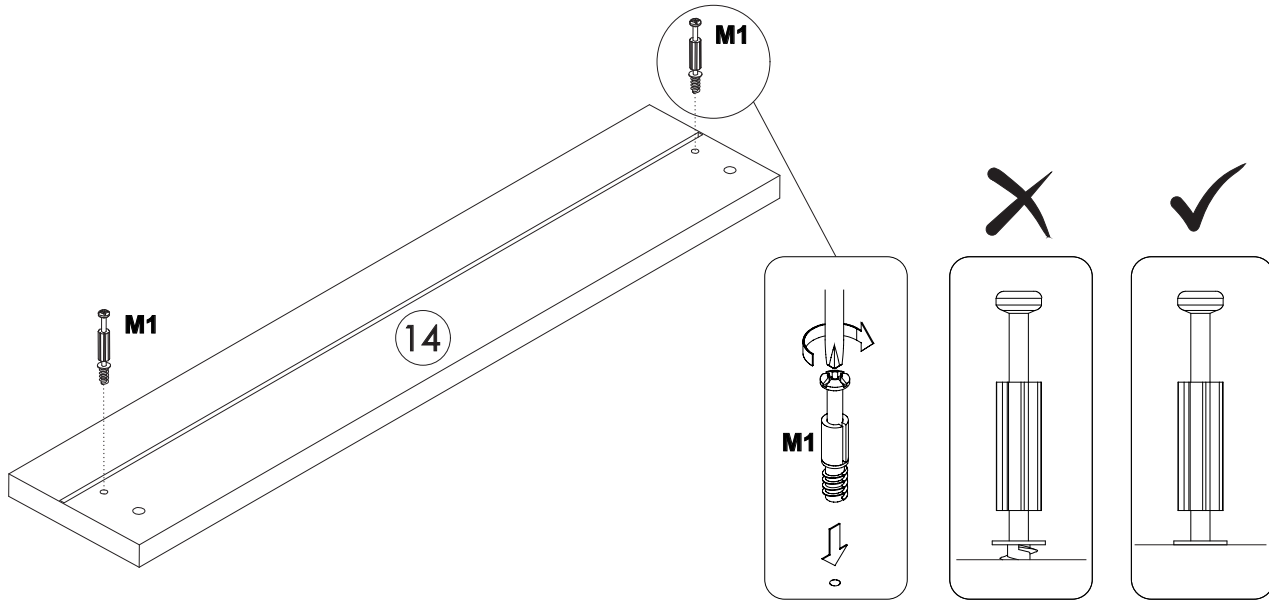
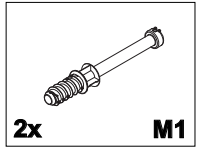
2x

31

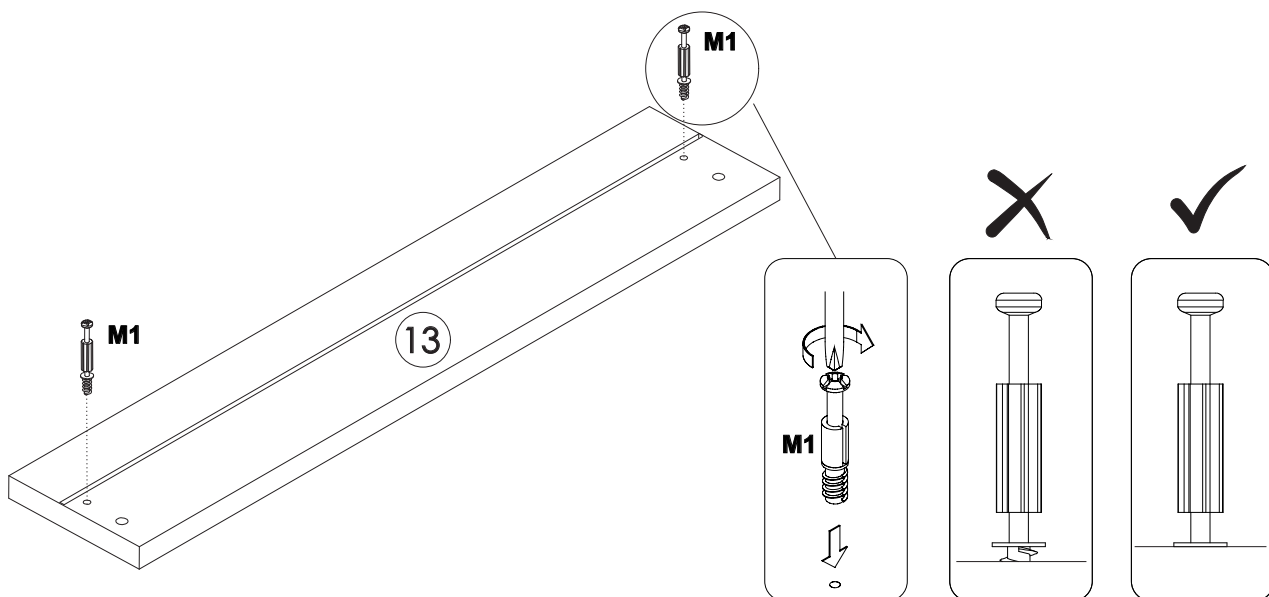
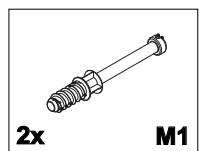


2x

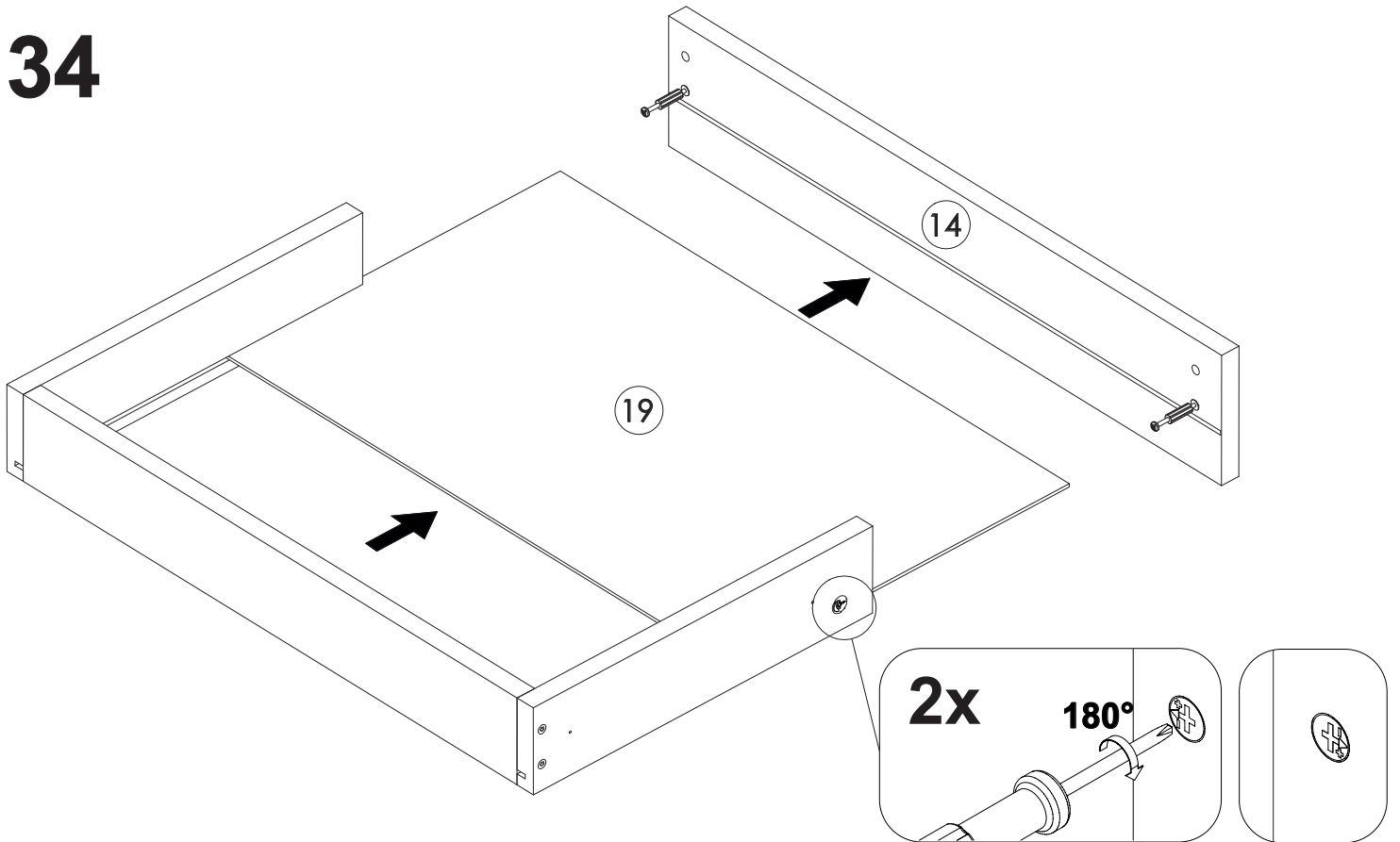
32



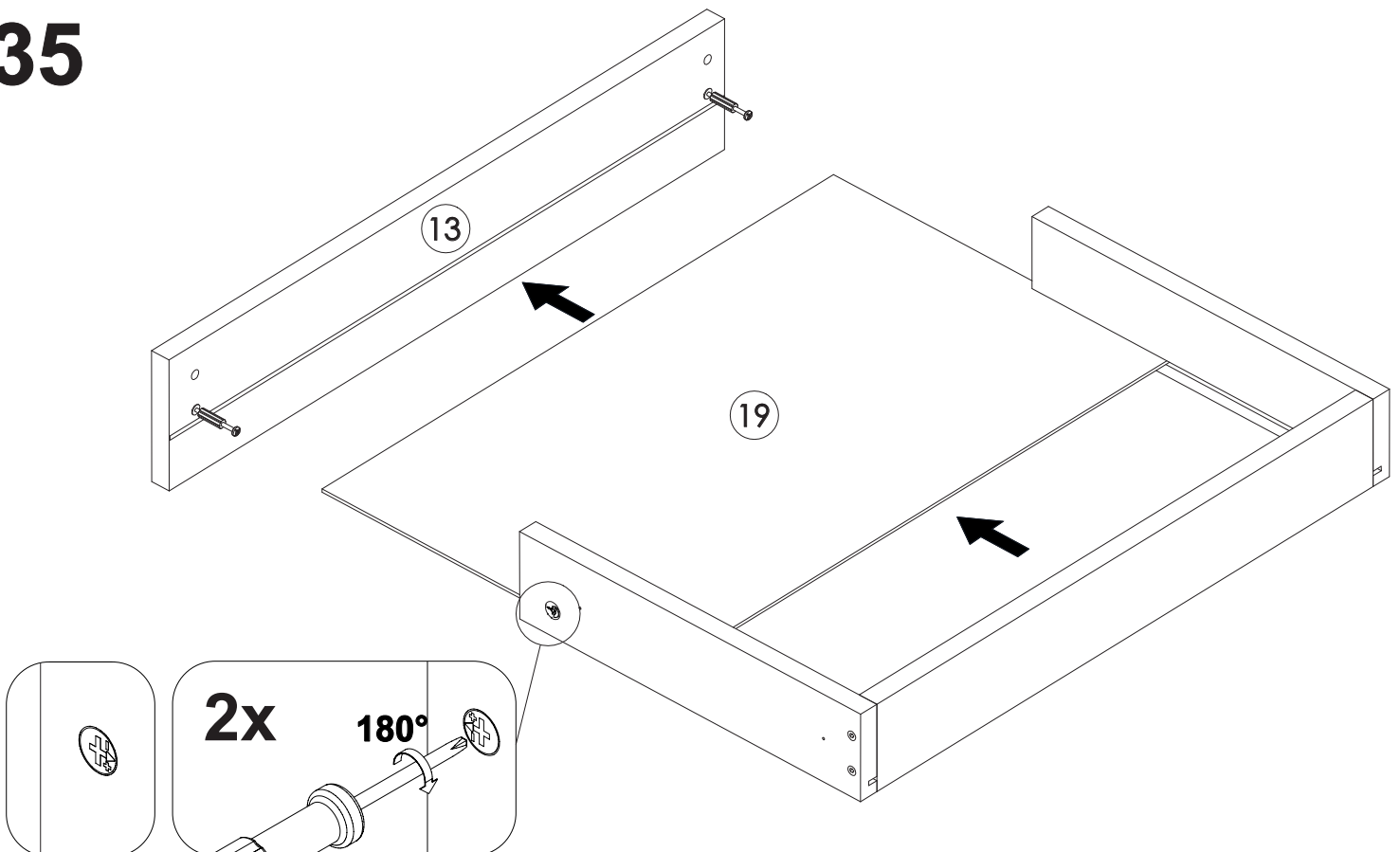
33



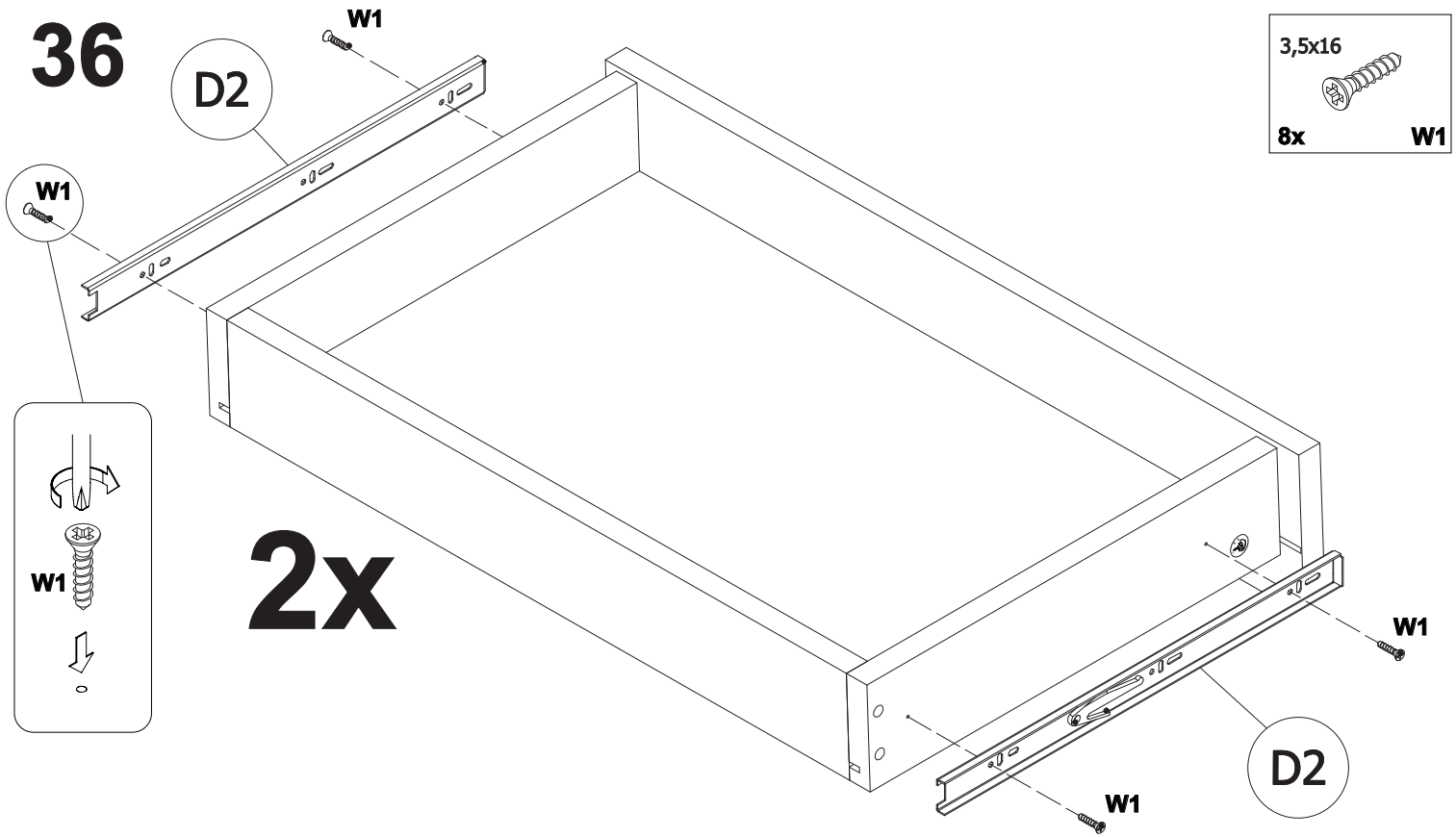
34



35



36



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